ML501

1.9–2.7 GHz High IP3 Mixer with Integrated LO Amp

The Communications Edge ™

Product Information

Product Features

- High dynamic range mixer with integrated LO driver
- +30 dBm Input IP3
- 8 dB Conversion Loss
- RF: 1900 – 2700 MHz
- LO: 1600 – 2500 MHz
- IF: 50 – 500 MHz
- 0 dBm Drive Level
- RoHS-compliant SOIC-8 pkg

Applications

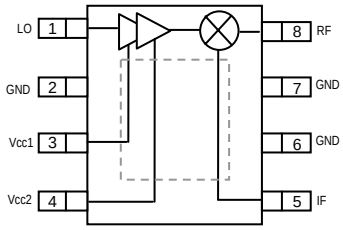
- 2.5/3G GSM/CDMA/WCDMA
- PCS/UMTS-band Mobile Infrastructure
- WiBro / WiLAN / WiMAX

Product Description

The ML501 high linearity upconverter or downconverter combines a passive GaAs MESFET mixer with an integrated HBT LO driver in a low-cost lead-free/green/RoHS-compliant SOIC-8 package. WJ's ML501 uses patented techniques to realize +30 dBm Input IP3 with 8 dB conversion loss using an LO drive level of 0 dBm in a downconverting application. The on-chip diplexer in the mixers allows for good matching on the RF and IF ports. The dual-stage LO driver provides a stable input power level into the mixer to allow for consistent performance over a wide range of LO power levels.

Typical applications include frequency up/down conversion, modulation and demodulation for receivers and transmitters used in 2.5G and 3G GSM/CDMA/WCDMA systems in the PCS, or UMTS frequency bands. They can also be used for WiBro/WiLAN/WiMAX infrastructure requiring high linearity frequency conversion.

Functional Diagram



The backside paddle is Ground.

Specifications (1)

Parameter	Units	Min	Typ	Max	Min	Typ	Max	Min	Typ	Max
RF Frequency Range	MHz		1900 – 2200			2200 – 2400			2400 – 2700	
LO Frequency Range	MHz		1600 – 2150			1900 – 2350			1900 – 2500	
IF Frequency Range	MHz		50 – 300			50 – 300			200 – 500	
SSB Conversion Loss	dB		8	9		8.1			8.4	
Input IP3 (2)	dBm	+28	+30			+30			+28	
Input P1dB	dBm		+21			+20			+20	
LO – RF Isolation (3)	dB		9			8			7	
LO – IF Isolation (3)	dB		27			27			25	
RF – IF Isolation	dB		20			21			21	
Return Loss: RF Port	dB		16			17			17	
Return Loss: IF Port	dB		20			20			20	
Return Loss: LO Port	dB		15			12			11	
LO Drive Level	dBm	-2.5	0	2.5	-2.5	0	2.5	-2.5	0	2.5
Supply Voltage	V		+5			+5			+5	
Operating Current (4)	mA	85	110	135		120			130	

1. Min / max limits are tested for the mixer in downconverting application with a low-side LO at 0 dBm at 25 °C with RF/IF = 1900/50, 2200/50, and 1900/300 MHz.
2. IIP3 is measured with Δf = 1 MHz with RFin = 0 dBm / tone.
3. LO is injected with 0 dBm.
4. This refers to the operating current under LO drive. The current can be reduced by increasing the value of the R2 resistor slightly.

Absolute Maximum Rating

Parameter	Rating
Operating Case Temperature	-40 to +85 °C
Storage Temperature	-55 to +150 °C
DC Voltage	+5.5 V
LO Power	+10 dBm
Input IF / RF Power	+20 dBm

Operation of this device above any of these parameters may cause permanent damage.

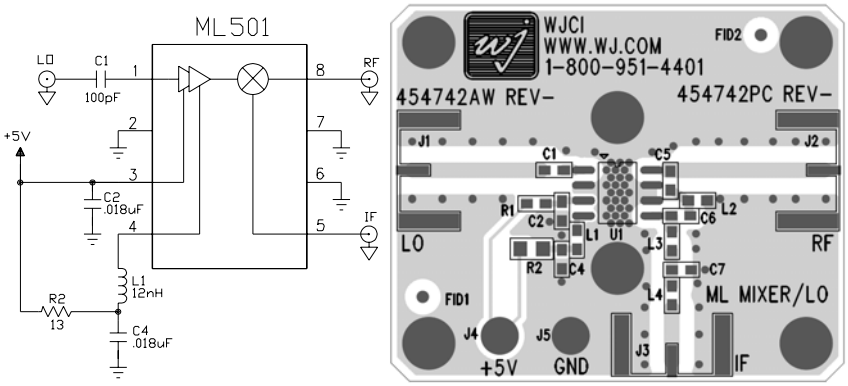
Ordering Information

Part No.	Description
ML501-G	1.9–2.7 GHz High IP3 Mixer w/ Integrated LO Amp (lead-free/green/RoHS-compliant SOIC-8 package)
ML501-PCB	Full Assembled Evaluation Board

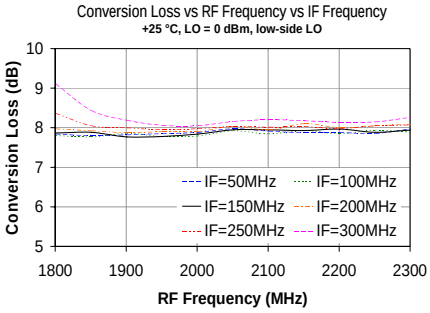
Specifications and information are subject to change without notice

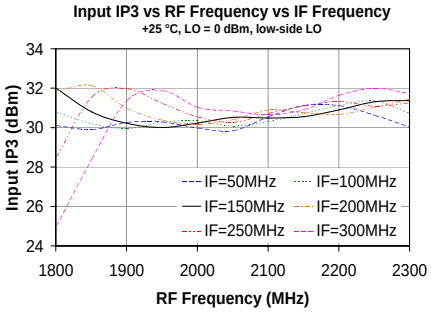
Typical Downconversion Performance Plots

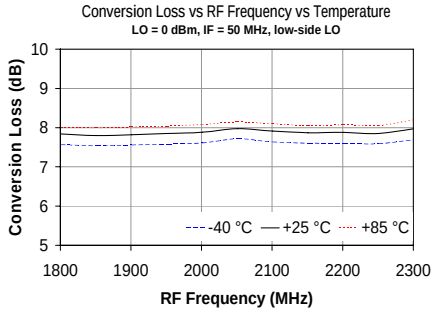
Performance using the circuitry on the ML501-PCB Evaluation Board

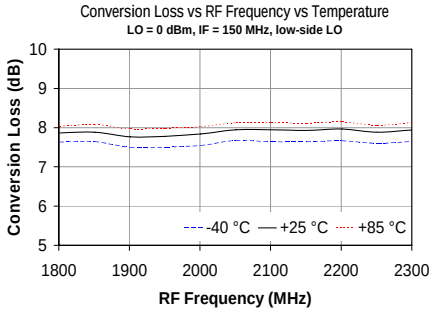


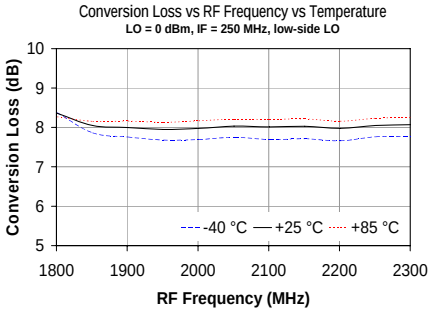
R1 is shown in the silkscreen but is not required for the ML501. A 0Ω jumper is placed in this spot on the PCB.

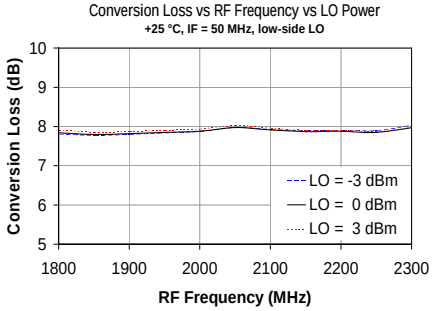


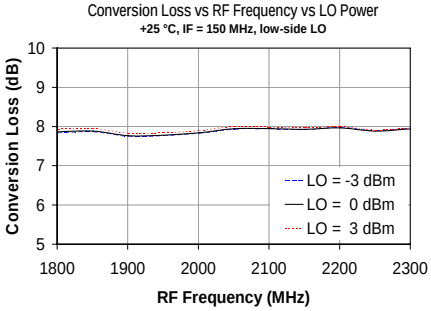


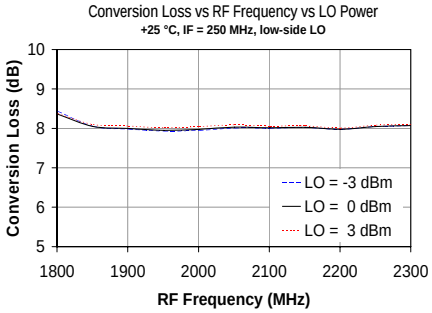








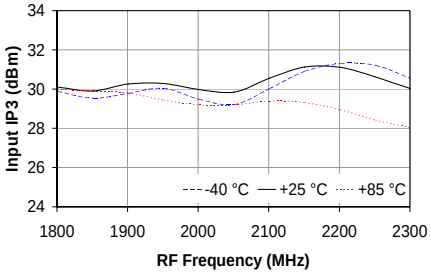




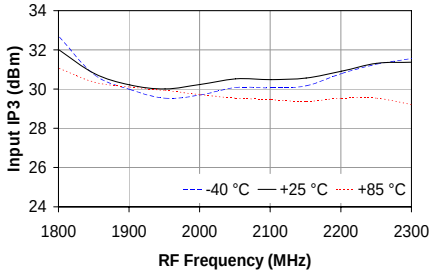
Typical Downconversion Performance Plots (cont'd)

Performance using the circuitry on the ML501-PCB Evaluation Board

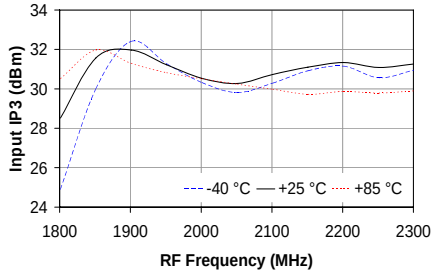
Input IP3 vs RF Frequency vs Temperature
LO = 0 dBm, IF = 50 MHz, low-side LO



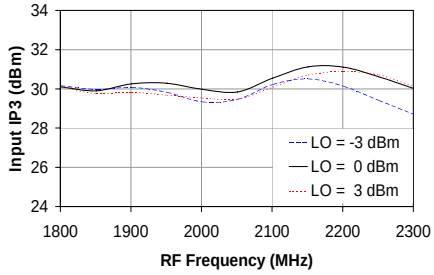
Input IP3 vs RF Frequency vs Temperature
LO = 0 dBm, IF = 150 MHz, low-side LO



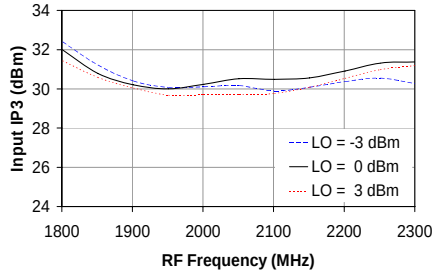
Input IP3 vs RF Frequency vs Temperature
LO = 0 dBm, IF = 250 MHz, low-side LO



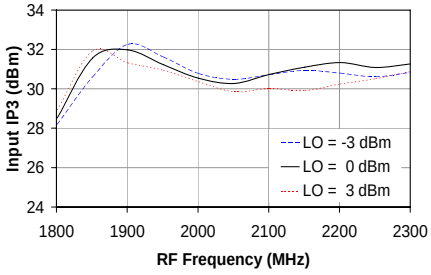
Input IP3 vs RF Frequency vs LO Power
+25 °C, IF = 50 MHz, low-side LO



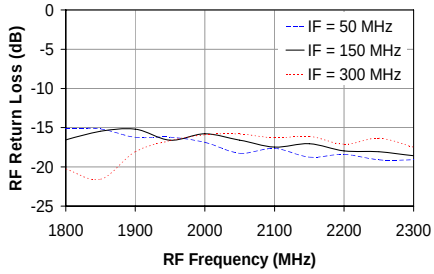
Input IP3 vs RF Frequency vs LO Power
+25 °C, IF = 150 MHz, low-side LO



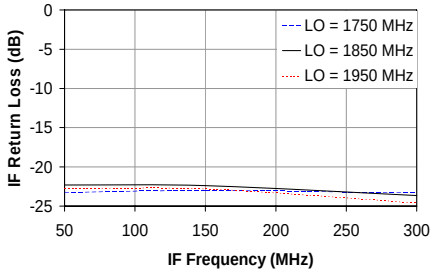
Input IP3 vs RF Frequency vs LO Power
+25 °C, IF = 250 MHz, low-side LO



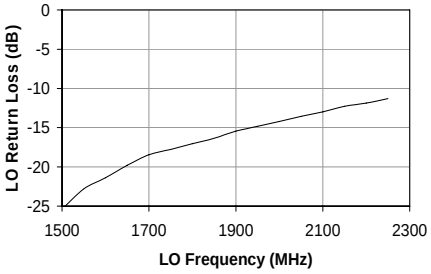
RF Return Loss vs LO Frequency
+25 °C, LO = 0 dBm



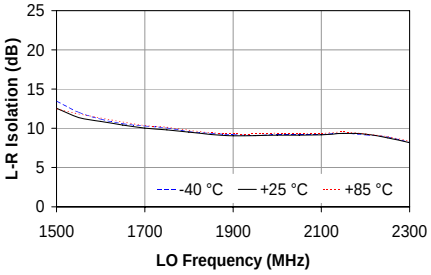
IF Return Loss vs IF Frequency
+25 °C, LO = 0 dBm



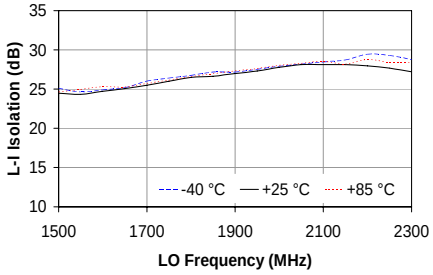
LO Return Loss vs LO Frequency
+25 °C



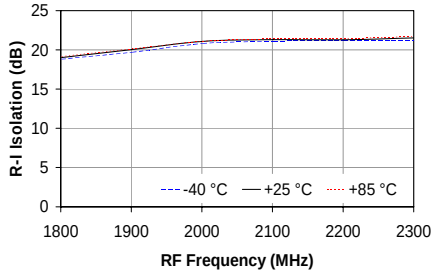
L-R Isolation vs LO Frequency
Referenced with LO = 0 dBm



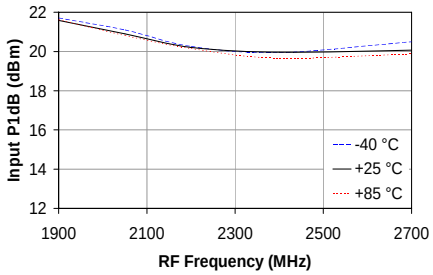
L-I Isolation vs LO Frequency
Referenced with LO = 0 dBm



R-I Isolation vs RF Frequency



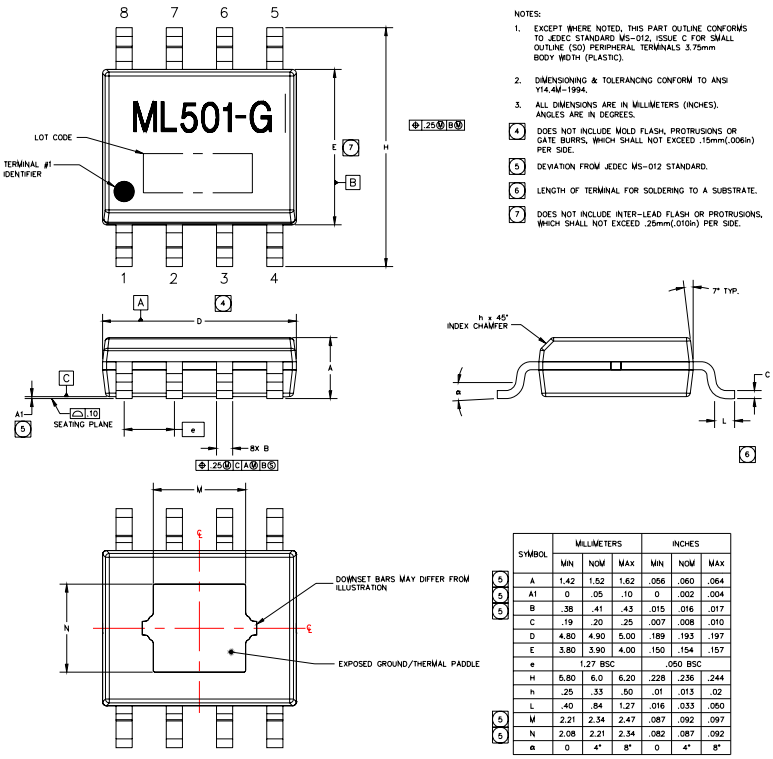
Input P1dB vs RF Frequency
IF = 100 MHz, low-side LO at 0 dBm



ML501-G Mechanical Information

This package is lead-free/green/RoHS-compliant. The plating material on the leads is NiPdAu. It is compatible with both lead-free (maximum 260 °C reflow temperature) and lead (maximum 245 °C reflow temperature) soldering processes.

Outline Drawing



NOTES:

1. EXCEPT WHERE NOTED, THIS PART OUTLINE CONFORMS TO JEDEC STANDARD MS-012, ISSUE C FOR SMALL OUTLINE (SO) PERIPHERAL TERMINALS 3.75mm BODY WIDTH (PLASTIC).
2. DIMENSIONING & TOLERANCING CONFORM TO ANSI Y14.4M-1994.
3. ALL DIMENSIONS ARE IN MILLIMETERS (INCHES). ANGLES ARE IN DEGREES.
4. DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS, WHICH SHALL NOT EXCEED .15mm(.006in) PER SIDE.
5. DEVIATION FROM JEDEC MS-012 STANDARD.
6. LENGTH OF TERMINAL FOR SOLDERING TO A SUBSTRATE.
7. DOES NOT INCLUDE INTER-LEAD FLASH OR PROTRUSIONS, WHICH SHALL NOT EXCEED .25mm(.010in) PER SIDE.

SYMBOL	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.42	1.52	1.62	.056	.060	.064
A1	0	.05	.10	0	.002	.004
B	.38	.41	.43	.015	.016	.017
C	.19	.20	.25	.007	.008	.010
D	4.80	4.90	5.00	.189	.193	.197
E	3.80	3.90	4.00	.150	.154	.157
e	1.27 BSC			.050 BSC		
H	5.80	6.0	6.20	.228	.236	.244
h	.25	.33	.50	.01	.013	.02
L	.40	.84	1.27	.016	.033	.050
M	2.21	2.34	2.47	.087	.092	.097
N	2.08	2.21	2.34	.082	.087	.092
α	0	4°	8°	0	4°	8°

Product Marking

The component will be lasermarked with a "ML501-G" product label with an alphanumeric lot code on the top surface of the package.

Tape and reel specifications for this part will be located on the website in the "Application Notes" section.

ESD / MSL Information

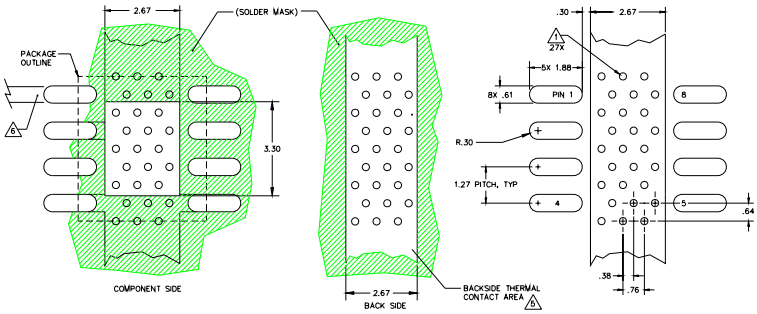
Caution! ESD sensitive device.

ESD Rating: Class 1B
Value: Passes ≥ 500V to <1000V
Test: Human Body Model (HBM)
Standard: JEDEC Standard JESD22-A114

ESD Rating: Class IV
Value: Passes ≥ 1000V
Test: Charged Device Model (CDM)
Standard: JEDEC Standard JESD22-C101

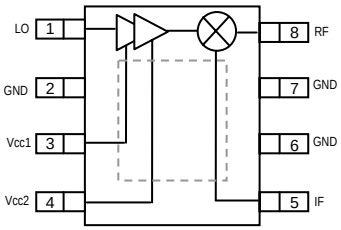
MSL Rating: Level 2 at +260 °C convection reflow
Standard: JEDEC Standard J-STD-020

Mounting Configuration / Land Pattern



1. Ground / thermal vias are critical for the proper performance of this device. Vias should use a .35mm (#80 / .0135") diameter drill and have a final plated thru diameter of .25 mm (.010").
2. Add as much copper as possible to inner and outer layers near the part to ensure optimal thermal performance.
3. Mounting screws can be added near the part to fasten the board to a heatsink. Ensure that the ground / thermal via region contacts the heatsink.
4. All dimensions are in millimeters (inches). Angles are in degrees.
5. Do not put solder mask on the backside of the PC board in the region where the board contacts the heatsink.
6. RF trace width depends upon the PC board material and construction.
7. Use 1 oz. Copper minimum.

Functional Pin Layout



The backside paddle is Ground.

Pin	Function
1	LO
2	GND
3	Vcc1
4	Vcc2
5	IF
6	GND
7	GND
8	RF

Backside paddle is RF and DC ground.

Thermal Specifications

Parameter	Rating
Operating Case Temperature	-40 to +85 °C
Thermal Resistance, Rth	104 °C / W

Specifications and information are subject to change without notice

О компании

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- Индивидуальный подход
- Гибкое ценообразование

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Большой выбор предлагаемой продукции, различные виды оплаты и доставки, позволят Вам сэкономить время и получить максимум выгоды от сотрудничества с нами!

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Lattice
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HOLTEK
International IOR Rectifier
LINEAR TECHNOLOGY
MITSUBISHI
National Semiconductor
ON Semiconductor
REALTEK
SANYO
SHINDENGEN
SGS
TECCOR
TUNDRA
XILINX
Amphenol
Bay Linear
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С уважением,

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«Трейд Электроникс»

Шишлаков Евгений

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manager28@tradeelectronics.ru

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