



## LM431 Adjustable Precision Zener Shunt Regulator

### 1 Features

- Average Temperature Coefficient 50 ppm/°C
- Temperature Compensated for Operation Over the Full Temperature Range
- Programmable Output Voltage
- Fast Turnon Response
- Low-Output Noise
- Low-Dynamic Output Impedance
- Available in Space-Saving SOIC-8, SOT-23, and TO-92 Packages

### 2 Applications

- Adjustable Voltage or Current Linear and Switching Power Supplies
- Voltage Monitoring
- Current Source and Sink Circuits
- Circuits Requiring Precision References
- Zener Diode Replacements

### 3 Description

The LM431 is a 3-terminal adjustable shunt regulator with ensured temperature stability over the entire temperature range of operation. The output voltage may be set at any level greater than 2.5 V ( $V_{REF}$ ) up to 36 V merely by selecting two external resistors that act as a voltage divided network. Due to the sharp turnon characteristics this device is an excellent replacement for many Zener diode applications.

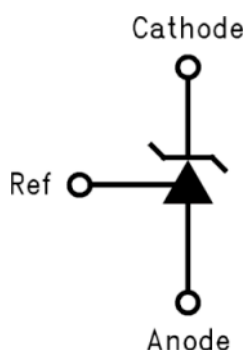
The LM431 is available in space-saving SOIC-8, SOT-23, and TO-92 packages.

**Device Information<sup>(1)</sup>**

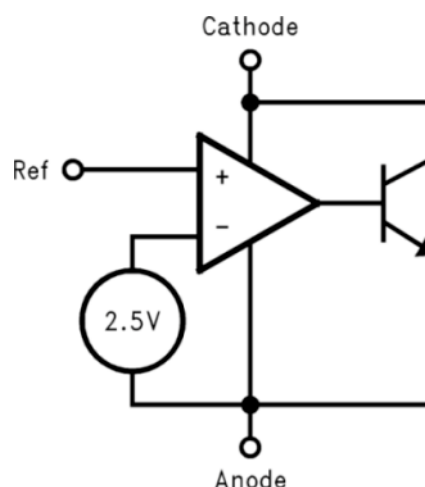
| PART NUMBER | PACKAGE    | BODY SIZE (NOM)   |
|-------------|------------|-------------------|
| LM431       | SOIC (8)   | 4.90 mm × 3.91 mm |
|             | SOT-23 (3) | 2.92 mm × 1.30 mm |
|             | TO-92 (3)  | 4.30 mm × 4.30 mm |

(1) For all available packages, see the orderable addendum at the end of the data sheet.

**LM431 Symbol**



**Functional Block Diagram**



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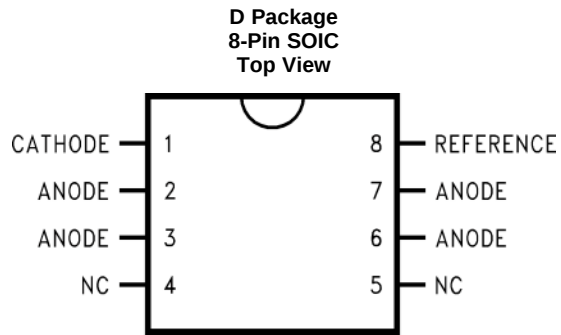
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## 4 Revision History

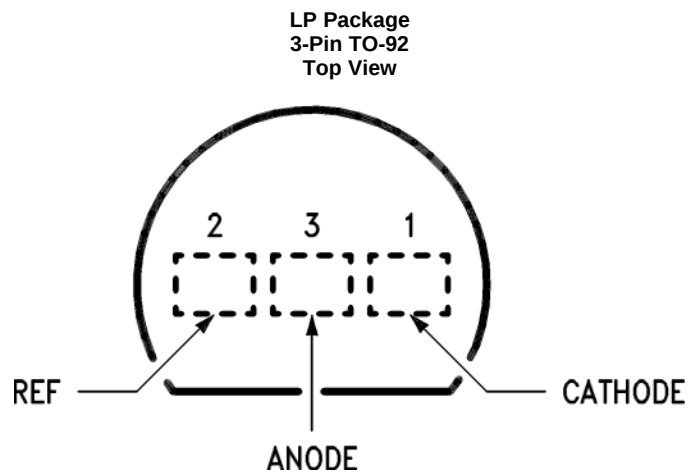
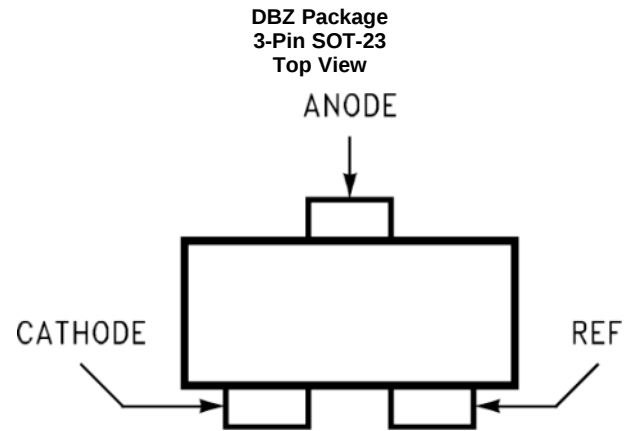
NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

| Changes from Revision G (March 2013) to Revision H                                                                                                                                                                                                                                                                                                                                                  | Page      |
|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------|
| <ul style="list-style-type: none"> <li>Added <i>ESD Ratings</i> table, <i>Feature Description</i> section, <i>Device Functional Modes</i>, <i>Application and Implementation</i> section, <i>Power Supply Recommendations</i> section, <i>Layout</i> section, <i>Device and Documentation Support</i> section, and <i>Mechanical, Packaging, and Orderable Information</i> section .....</li> </ul> | <b>1</b>  |
| Changes from Revision F (April 2013) to Revision G                                                                                                                                                                                                                                                                                                                                                  | Page      |
| <ul style="list-style-type: none"> <li>Changed layout of National Data Sheet to TI format .....</li> </ul>                                                                                                                                                                                                                                                                                          | <b>18</b> |

## 5 Pin Configuration and Functions



Note: NC = Not internally connected.



### Pin Functions

| NAME      | PIN        |        |       | I/O | DESCRIPTION                                 |
|-----------|------------|--------|-------|-----|---------------------------------------------|
|           | SOIC       | SOT-23 | TO-92 |     |                                             |
| Anode     | 2, 3, 6, 7 | 3      | 3     | O   | Anode pin, normally grounded                |
| Cathode   | 1          | 1      | 1     | I/O | Shunt current/output voltage                |
| NC        | 4, 5       | —      | —     | —   | No connect                                  |
| Reference | 8          | 2      | 2     | I   | Reference pin for adjustable output voltage |

## 6 Specifications

### 6.1 Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted)<sup>(1)(2)</sup>

|                                              |                      | MIN  | MAX | UNIT |
|----------------------------------------------|----------------------|------|-----|------|
| Cathode voltage                              |                      | 37   |     | V    |
| Reference voltage                            |                      | –0.5 |     | V    |
| Continuous cathode current                   |                      | –10  | 150 | mA   |
| Reference input current                      |                      | 10   |     | mA   |
| Internal power dissipation <sup>(3)(4)</sup> | TO-92 package        | 0.78 |     | W    |
|                                              | SOIC package         | 0.81 |     | W    |
|                                              | SOT-23 package       | 0.28 |     | W    |
| Operating temperature                        | Industrial (LM431xI) | –40  | 85  | °C   |
|                                              | Commercial (LM431xC) | 0    | 70  | °C   |
| Storage temperature                          |                      | –65  | 150 | °C   |

- (1) Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions*. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) If Military/Aerospace specified devices are required, please contact the TI Sales Office/ Distributors for availability and specifications.
- (3)  $T_{J\text{ Max}} = 150^{\circ}\text{C}$ .
- (4) Ratings apply to ambient temperature at  $25^{\circ}\text{C}$ . Above this temperature, derate the TO-92 at  $6.2\text{ mW}/^{\circ}\text{C}$ , the SOIC at  $6.5\text{ mW}/^{\circ}\text{C}$ , the SOT-23 at  $2.2\text{ mW}/^{\circ}\text{C}$ .

### 6.2 ESD Ratings

|                  |                         |                                                                   | VALUE | UNIT |
|------------------|-------------------------|-------------------------------------------------------------------|-------|------|
| $V_{\text{ESD}}$ | Electrostatic discharge | Human-body model (HBM), per ANSI/ESDA/JEDEC JS-001 <sup>(1)</sup> | ±2500 | V    |

- (1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

### 6.3 Recommended Operating Conditions

over operating free-air temperature range (unless otherwise noted)

|                 |  | MIN              | MAX | UNIT |
|-----------------|--|------------------|-----|------|
| Cathode voltage |  | $V_{\text{REF}}$ | 37  | V    |
| Cathode current |  | 1                | 100 | mA   |

### 6.4 Thermal Information

| THERMAL METRIC <sup>(1)</sup> |                                              | LM431    |              |            | UNIT |
|-------------------------------|----------------------------------------------|----------|--------------|------------|------|
|                               |                                              | D (SOIC) | DBZ (SOT-23) | LP (TO-92) |      |
|                               |                                              | 8 PINS   | 3 PINS       | 3 PINS     |      |
| $R_{\theta\text{JA}}$         | Junction-to-ambient thermal resistance       | 126.9    | 267.7        | 162.4      | °C/W |
| $R_{\theta\text{JC(top)}}$    | Junction-to-case (top) thermal resistance    | 72.2     | 138.3        | 85.8       | °C/W |
| $R_{\theta\text{JB}}$         | Junction-to-board thermal resistance         | 67.5     | 61           | —          | °C/W |
| $\Psi_{\text{JT}}$            | Junction-to-top characterization parameter   | 21.1     | 21.5         | 29.4       | °C/W |
| $\Psi_{\text{JB}}$            | Junction-to-board characterization parameter | 67       | 60.1         | 141.5      | °C/W |
| $R_{\theta\text{JC(bot)}}$    | Junction-to-case (bottom) thermal resistance | —        | —            | —          | °C/W |

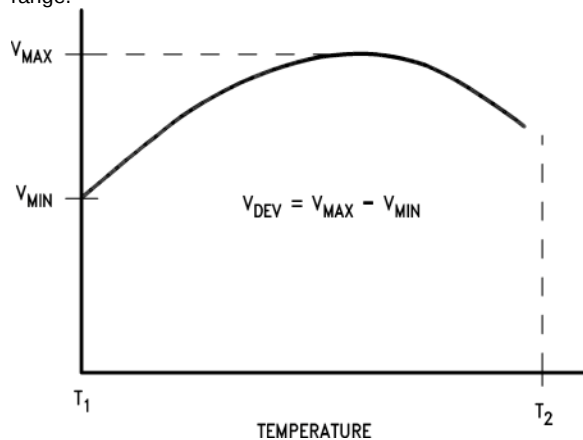
- (1) For more information about traditional and new thermal metrics, see the *Semiconductor and IC Package Thermal Metrics* application report, [SPRA953](#).

## 6.5 Electrical Characteristics

T<sub>A</sub> = 25°C unless otherwise specified

| PARAMETER                              |                                                                              | TEST CONDITIONS                                                                                                |                                              | MIN   | TYP   | MAX  | UNIT |
|----------------------------------------|------------------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------------|----------------------------------------------|-------|-------|------|------|
| V <sub>REF</sub>                       | Reference voltage                                                            | V <sub>Z</sub> = V <sub>REF</sub> , I <sub>I</sub> = 10 mA<br>LM431A (Figure 6 )                               |                                              | 2.44  | 2.495 | 2.55 | V    |
|                                        |                                                                              | V <sub>Z</sub> = V <sub>REF</sub> , I <sub>I</sub> = 10 mA<br>LM431B (Figure 6 )                               |                                              | 2.47  | 2.495 | 2.52 |      |
|                                        |                                                                              | V <sub>Z</sub> = V <sub>REF</sub> , I <sub>I</sub> = 10 mA<br>LM431C (Figure 6 )                               |                                              | 2.485 | 2.5   | 2.51 |      |
| V <sub>DEV</sub>                       | Deviation of reference input voltage<br>overtemperature <sup>(1)</sup>       | V <sub>Z</sub> = V <sub>REF</sub> , I <sub>I</sub> = 10 mA,<br>T <sub>A</sub> = full range (Figure 6 )         |                                              |       | 8     | 17   | mV   |
| ΔV <sub>REF</sub> /<br>ΔV <sub>Z</sub> | Ratio of the change in reference voltage to the<br>change in cathode voltage | I <sub>Z</sub> = 10 mA<br>(Figure 7 )                                                                          | V <sub>Z</sub> from V <sub>REF</sub> to 10 V |       | -1.4  | -2.7 | mV/V |
|                                        |                                                                              |                                                                                                                | V <sub>Z</sub> from 10 V to 36 V             |       | -1    | -2   |      |
| I <sub>REF</sub>                       | Reference input current                                                      | R <sub>1</sub> = 10 kΩ, R <sub>2</sub> = ∞, I <sub>I</sub> = 10 mA<br>(Figure 7 )                              |                                              |       | 2     | 4    | μA   |
| ∞I <sub>REF</sub>                      | Deviation of reference input current<br>overtemperature                      | R <sub>1</sub> = 10 kΩ, R <sub>2</sub> = ∞, I <sub>I</sub> = 10 mA,<br>T <sub>A</sub> = full range (Figure 7 ) |                                              |       | 0.4   | 1.2  | μA   |
| I <sub>Z(MIN)</sub>                    | Minimum cathode current for regulation                                       | V <sub>Z</sub> = V <sub>REF</sub> (Figure 6 )                                                                  |                                              |       | 0.4   | 1    | mA   |
| I <sub>Z(OFF)</sub>                    | OFF-state current                                                            | V <sub>Z</sub> = 36 V, V <sub>REF</sub> = 0 V (Figure 8)                                                       |                                              |       | 0.3   | 1    | μA   |

- (1) Deviation of reference input voltage, V<sub>DEV</sub>, is defined as the maximum variation of the reference input voltage over the full temperature range.



The average temperature coefficient of the reference input voltage, ∞V<sub>REF</sub>, is defined as:

$$\infty V_{REF} \frac{\text{ppm}}{^{\circ}\text{C}} = \frac{\pm \left[ \frac{V_{\text{Max}} - V_{\text{Min}}}{V_{\text{REF}} (\text{at } 25^{\circ}\text{C})} \right] 10^6}{T_2 - T_1} = \frac{\pm \left[ \frac{V_{\text{DEV}}}{V_{\text{REF}} (\text{at } 25^{\circ}\text{C})} \right] 10^6}{T_2 - T_1}$$

Where:

T<sub>2</sub> – T<sub>1</sub> = full temperature change (0–70°C).

V<sub>REF</sub> can be positive or negative depending on whether the slope is positive or negative.

Example: V<sub>DEV</sub> = 8 mV, V<sub>REF</sub> = 2495 mV, T<sub>2</sub> – T<sub>1</sub> = 70°C, slope is positive.

$$\infty V_{REF} = \frac{\left[ \frac{8.0 \text{ mV}}{2495 \text{ mV}} \right] 10^6}{70^{\circ}\text{C}} = +46 \text{ ppm}/^{\circ}\text{C}$$

## Electrical Characteristics (continued)

T<sub>A</sub> = 25°C unless otherwise specified

| PARAMETER                                              | TEST CONDITIONS                                                                   | MIN | TYP | MAX  | UNIT |
|--------------------------------------------------------|-----------------------------------------------------------------------------------|-----|-----|------|------|
| r <sub>Z</sub> Dynamic output impedance <sup>(2)</sup> | V <sub>Z</sub> = V <sub>REF</sub> , LM431A,<br>Frequency = 0 Hz (Figure 6)        |     |     | 0.75 | Ω    |
|                                                        | V <sub>Z</sub> = V <sub>REF</sub> , LM431B, LM431C<br>Frequency = 0 Hz (Figure 6) |     |     | 0.5  |      |

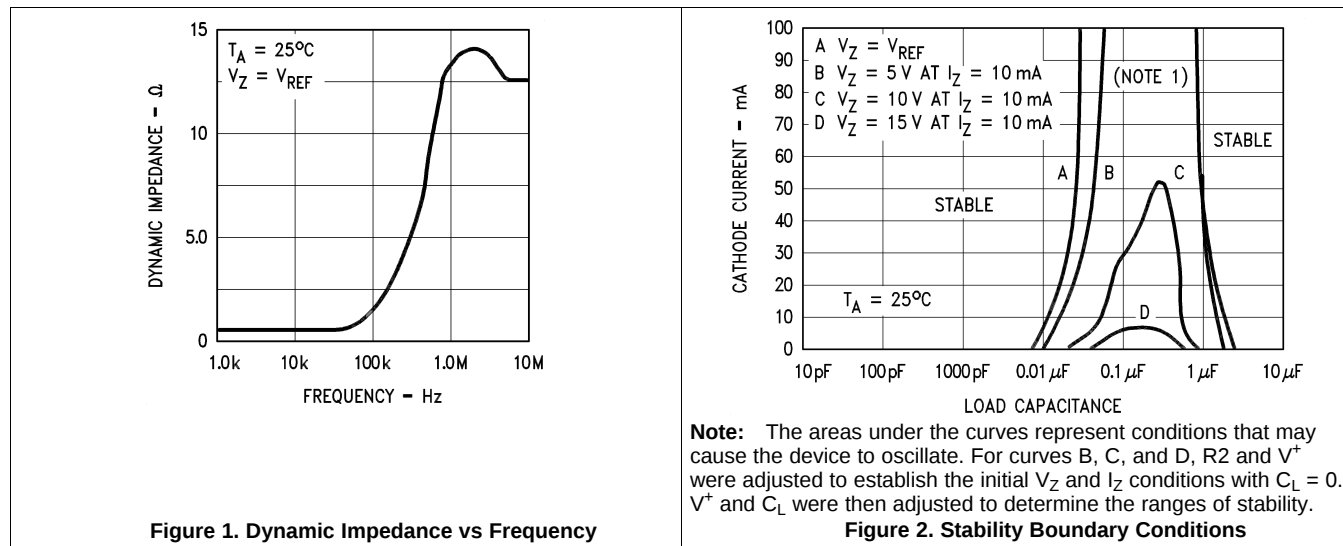
(2) The dynamic output impedance, r<sub>Z</sub>, is defined as:

$$r_Z = \frac{\Delta V_Z}{\Delta I_Z}$$

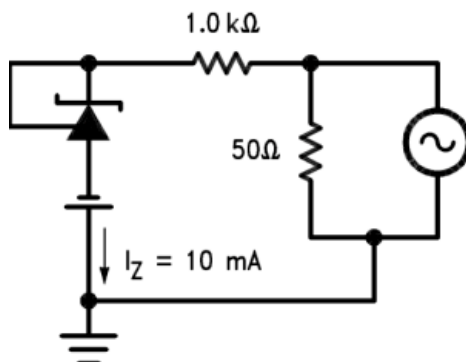
When the device is programmed with two external resistors, R1 and R2, (see Figure 7), the dynamic output impedance of the overall circuit, r<sub>Z</sub>, is defined as:

$$r_Z = \frac{\Delta V_Z}{\Delta I_Z} \cong \left[ r_Z \left( 1 + \frac{R1}{R2} \right) \right]$$

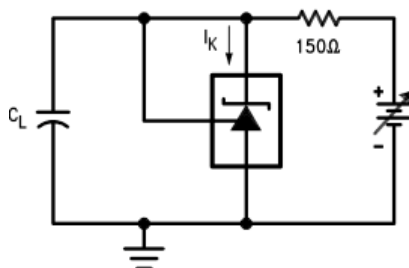
## 6.6 Typical Characteristics



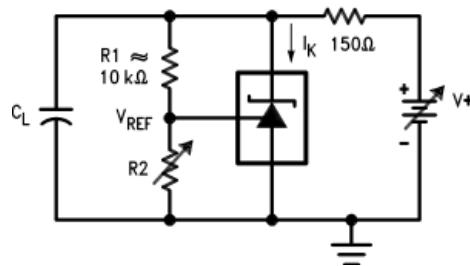
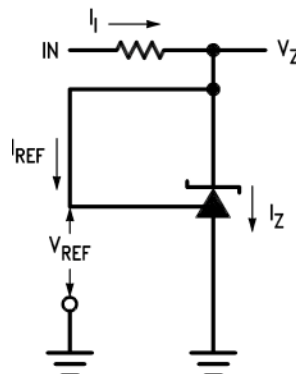
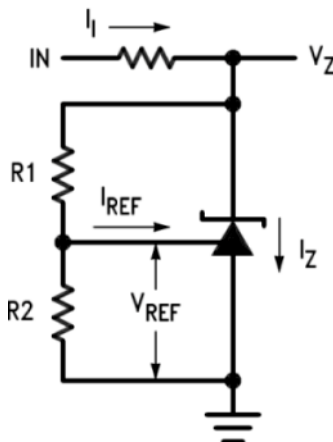
## 7 Parameter Measurement Information



**Figure 3. Test Circuit for Dynamic Impedance vs Frequency Curve**



**Figure 4. Test Circuit for Curve A Above**

**Parameter Measurement Information (continued)**

**Figure 5. Test Circuit for Curves B, C and D Above**

**Figure 6. Test Circuit for  $V_Z = V_{REF}$** 


**Note:**  $V_Z = V_{REF} (1 + R1/R2) + I_{REF} \times R1$

**Figure 7. Test Circuit for  $V_Z > V_{REF}$**

## Parameter Measurement Information (continued)

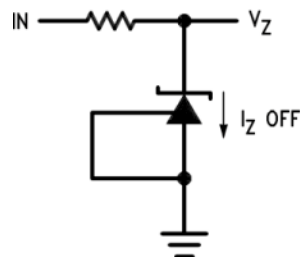


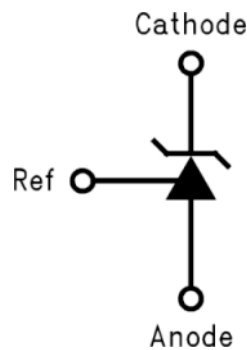
Figure 8. Test Circuit for OFF-State Current

## 8 Detailed Description

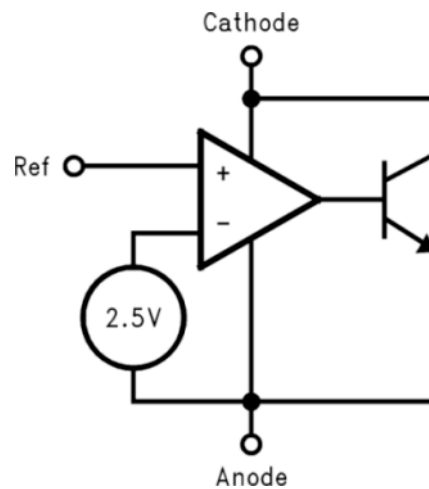
### 8.1 Overview

The LM431 is an adjustable precision shunt voltage regulator with ensured temperature stability over the entire temperature range. The part has three different packages available to meet small footprint requirements, and is available in three different tolerance grades.

### 8.2 Functional Block Diagram



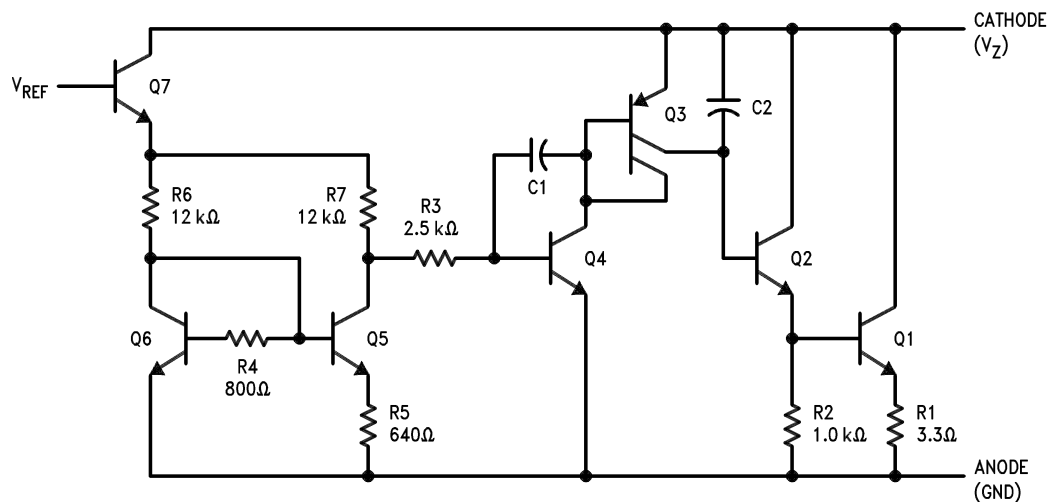
**Figure 9. LM431 Symbol**



**Figure 10. LM431 Block Diagram**

### 8.3 Feature Description

The LM431 is a precision Zener diode. The part requires a small quiescent current for regulation, and regulates the output voltage by shunting more or less current to ground, depending on input voltage and load. The only external component requirement is a resistor between the cathode and the input voltage to set the input current. An external capacitor can be used on the input or output, but is not required.



## 9 Application and Implementation

### NOTE

Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes. Customers must validate and test their design implementation to confirm system functionality.

### 9.1 Application Information

The LM431 is an adjustable precision shunt voltage regulator with ensured temperature stability over the entire temperature range. For space critical applications, the LM431 is available in space saving SOIC-8, SOT-23 and TO-92 packages. The minimum operating current is 1 mA while the maximum operating current is 100 mA.

The typical thermal hysteresis specification is defined as the change in 25°C voltage measured after thermal cycling. The device is thermal cycled to temperature 0°C and then measured at 25°C. Next the device is thermal cycled to temperature 70°C and again measured at 25°C. The resulting VOUT delta shift between the 25°C measurements is thermal hysteresis. Thermal hysteresis is common in precision references and is induced by thermal-mechanical package stress. Changes in environmental storage temperature, operating temperature and board mounting temperature are all factors that can contribute to thermal hysteresis.

In a conventional shunt regulator application ([Figure 12](#)), an external series resistor ( $R_S$ ) is connected between the supply voltage and the LM431 cathode pin.  $R_S$  determines the current that flows through the load ( $I_{LOAD}$ ) and the LM431 ( $I_Z$ ). Since load current and supply voltage may vary,  $R_S$  must be small enough to supply at least the minimum acceptable  $I_Z$  to the LM431 even when the supply voltage is at its minimum and the load current is at its maximum value. When the supply voltage is at its maximum and  $I_{LOAD}$  is at its minimum,  $R_S$  must be large enough so that the current flowing through the LM431 is less than 100 mA.

$R_S$  must be selected based on the supply voltage, ( $V_+$ ), the desired load and operating current, ( $I_{LOAD}$  and  $I_Z$ ), and the output voltage, see [Equation 1](#).

$$R_S = \frac{V_+ - V_O}{I_{LOAD} + I_Z} \quad (1)$$

The LM431 output voltage can be adjusted to any value in the range of 2.5 V through 37 V. It is a function of the internal reference voltage ( $V_{REF}$ ) and the ratio of the external feedback resistors as shown in [Figure 12](#). The output voltage is found using [Equation 2](#).

$$V_O = V_{REF} * (1 + R_1/R_2)$$

where

- $V_O$  is the output voltage (also, cathode voltage,  $V_Z$ ). The actual value of the internal  $V_{REF}$  is a function of  $V_Z$ . (2)

The corrected  $V_{REF}$  is determined by [Equation 3](#):

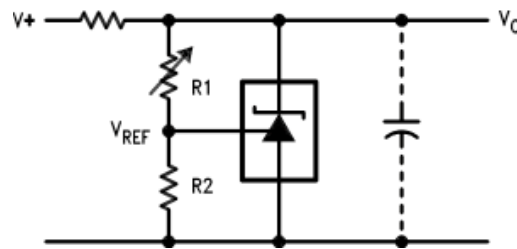
$$V_{REF} = \Delta V_Z * (\Delta V_{REF}/\Delta V_Z) + V_Y$$

where

- $V_Y = 2.5$  V and  $\Delta V_Z = (V_Z - V_Y)$
- $\Delta V_{REF}/\Delta V_Z$  is found in the [Electrical Characteristics](#) and is typically  $-1.4$  mV/V for  $V_Z$  raging from  $V_{REF}$  to 10 V and  $-1$  mV/V for  $V_Z$  raging from 10 V to 36 V. (3)

## 9.2 Typical Applications

### 9.2.1 Shunt Regulator



$$V_O \approx \left(1 + \frac{R_1}{R_2}\right) V_{REF}$$

Figure 12. Shunt Regulator

#### 9.2.1.1 Design Requirements

Design a shunt regulator with the following requirements:

- $V_+ > V_O$
- $V_O = 5\text{ V}$

Select  $R_S$  (a resistor between  $V_+$  and  $V_O$ ) such that:  $1\text{ mA} < I_Z < 100\text{ mA}$

#### 9.2.1.2 Detailed Design Procedure

The resistor  $R_S$  must be selected such that current  $I_Z$  remains in the operational region of the part for the entire  $V_+$  range and load current range. The two extremes to consider are  $V_+$  at its minimum, and the load at its maximum, where  $R_S$  must be small enough for  $I_Z$  to remain above  $1\text{ mA}$ . The other extreme is  $V_+$  at its maximum, and the load at its minimum, where  $R_S$  must be large enough to maintain  $I_Z < 100\text{ mA}$ . If unsure, try using  $1\text{ mA} \leq I_R \leq 10\text{ mA}$  as a starting point; just remember the value of  $I_Z$  varies with input voltage and load.

Use Equation 4 and Equation 5 to set  $R_S$  between  $R_{S\_MIN}$  and  $R_{S\_MAX}$ .

$$R_{S\_MIN} = \frac{V_{+\_MAX} - V_O}{I_{LOAD\_MIN} + I_{Z\_MAX}} \quad (4)$$

$$R_{S\_MAX} = \frac{V_{+\_MIN} - V_O}{I_{LOAD\_MAX} + I_{Z\_MIN}} \quad (5)$$

Set feedback resistors  $R_1$  and  $R_2$  for a resistor divider based on Equation 2 and reproduced in Equation 6

$$V_O = V_{REF} * (1 + R_1/R_2) \quad (6)$$

So, for a 5-V output voltage,  $V_O$ , and  $V_{REF}$  of 2.5 V, simple calculation yields  $R_1/R_2 = 1$ . Based on this, select  $R_1 = 1\text{ k}\Omega$  and  $R_2 = 1\text{ k}\Omega$ .

## Typical Applications (continued)

### 9.2.1.3 Application Curves

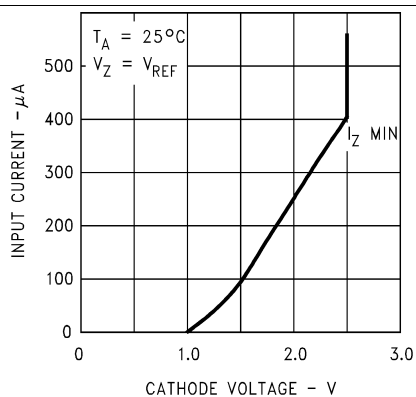


Figure 13. Input Current vs  $V_Z$

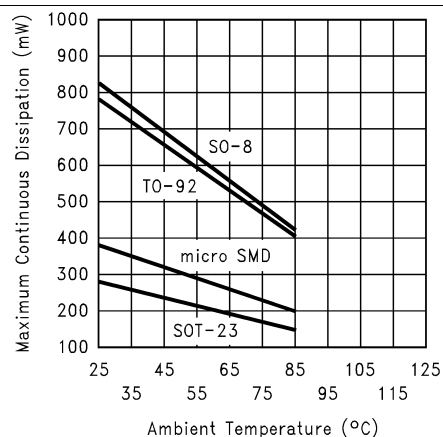


Figure 14. Thermal Information

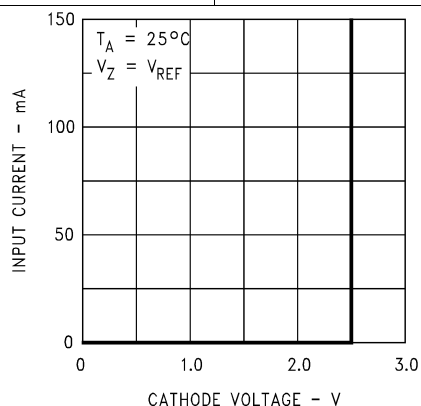


Figure 15. Input Current vs  $V_Z$

### 9.2.2 Other Applications

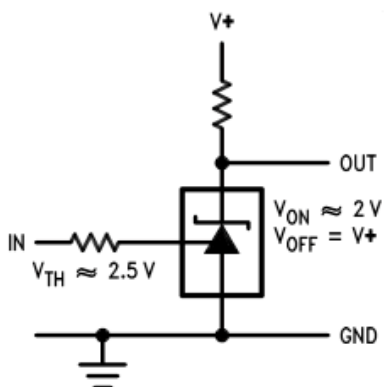


Figure 16. Single Supply Comparator With Temperature Compensated Threshold

## Typical Applications (continued)

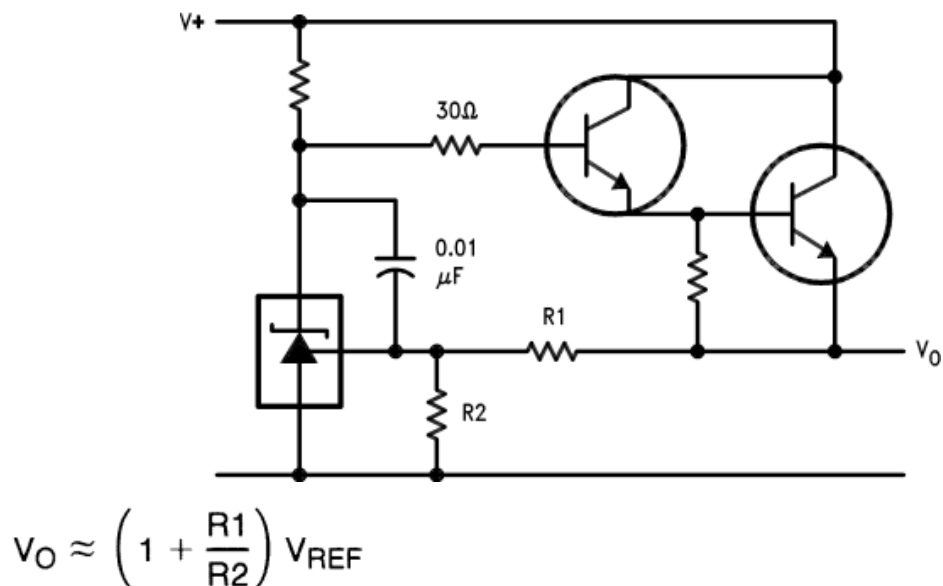


Figure 17. Series Regulator

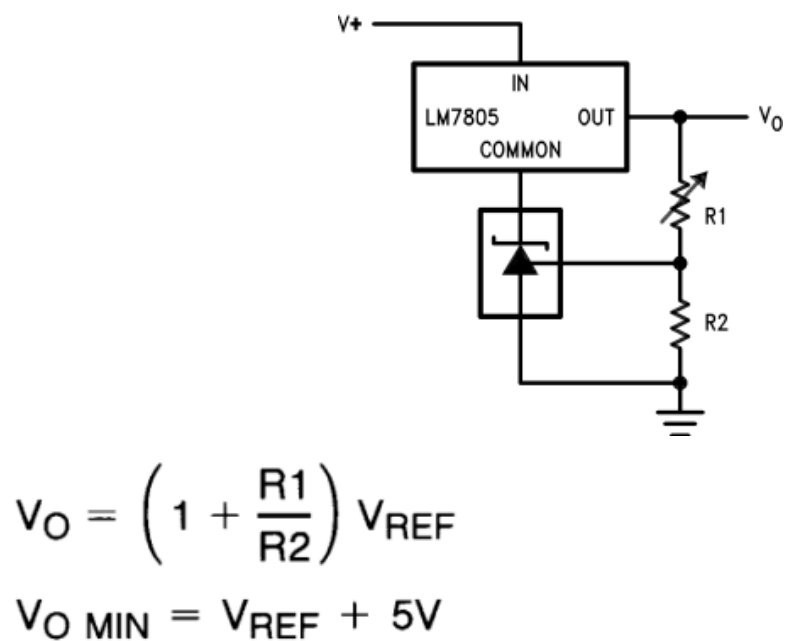
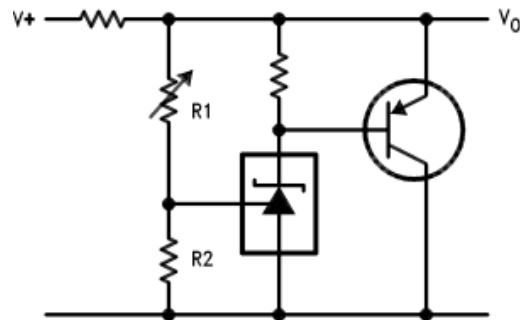


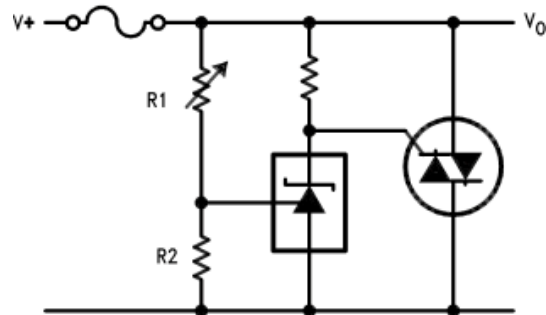
Figure 18. Output Control of a Three Terminal Fixed Regulator

## Typical Applications (continued)



$$V_O \approx \left( 1 + \frac{R1}{R2} \right) V_{REF}$$

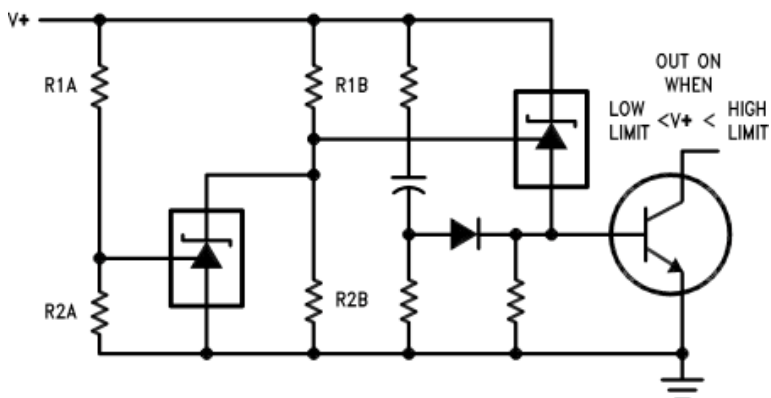
**Figure 19. Higher Current Shunt Regulator**



$$V_{LIMIT} \approx \left( 1 + \frac{R1}{R2} \right) V_{REF}$$

**Figure 20. Crow Bar**

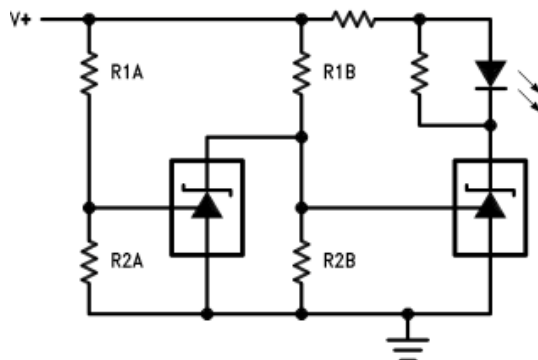
## Typical Applications (continued)



$$\text{LOW LIMIT} \approx V_{\text{REF}} \left( 1 + \frac{R1B}{R2B} \right) + V_{\text{BE}}$$

$$\text{HIGH LIMIT} \approx V_{\text{REF}} \left( 1 + \frac{R1A}{R2A} \right)$$

Figure 21. Over Voltage and Under Voltage Protection Circuit



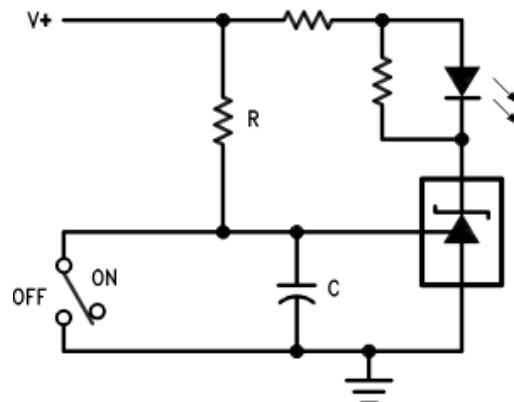
$$\text{LOW LIMIT} \approx V_{\text{REF}} \left( 1 + \frac{R1B}{R2B} \right)$$

$$\text{HIGH LIMIT} \approx V_{\text{REF}} \left( 1 + \frac{R1A}{R2A} \right)$$

LED ON WHEN  
LOW LIMIT < V<sup>+</sup> < HIGH LIMIT

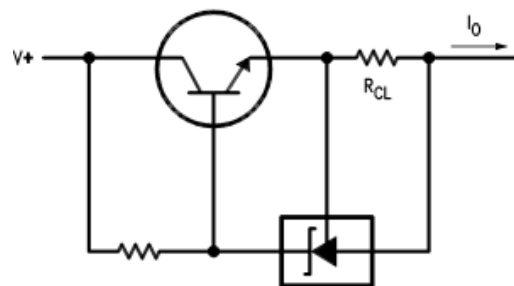
Figure 22. Voltage Monitor

## Typical Applications (continued)



$$\text{DELAY} = R \cdot C \cdot \ln \frac{V^+}{(V^+) - V_{\text{REF}}}$$

Figure 23. Delay Timer



$$I_O = \frac{V_{\text{REF}}}{R_{\text{CL}}}$$

Figure 24. Current Limiter or Current Source

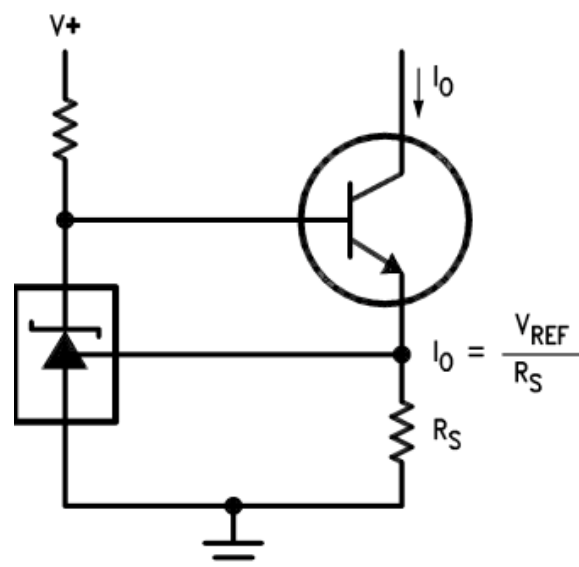


Figure 25. Constant Current Sink

## 10 Power Supply Recommendations

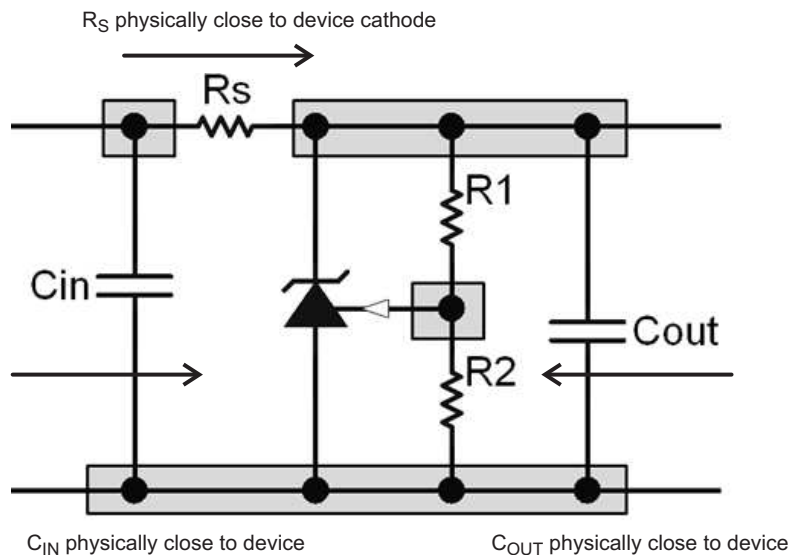
While a bypass capacitor is not required on the input voltage line, TI recommends reducing noise on the input which could affect the output. TI recommends a 0.1- $\mu$ F ceramic capacitor or larger.

## 11 Layout

### 11.1 Layout Guidelines

Place external components as close to the device as possible. Place  $R_S$  close to the cathode, as well as the input bypass capacitor, if used. Keep feedback resistor close the device whenever possible.

### 11.2 Layout Example



**Figure 26. LM431 Layout Recommendation**

## 12 Device and Documentation Support

### 12.1 Community Resources

The following links connect to TI community resources. Linked contents are provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's [Terms of Use](#).

**TI E2E™ Online Community** *TI's Engineer-to-Engineer (E2E) Community*. Created to foster collaboration among engineers. At [e2e.ti.com](http://e2e.ti.com), you can ask questions, share knowledge, explore ideas and help solve problems with fellow engineers.

**Design Support** *TI's Design Support* Quickly find helpful E2E forums along with design support tools and contact information for technical support.

### 12.2 Trademarks

E2E is a trademark of Texas Instruments.  
All other trademarks are the property of their respective owners.

### 12.3 Electrostatic Discharge Caution



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

### 12.4 Glossary

[SLYZ022](#) — *TI Glossary*.

This glossary lists and explains terms, acronyms, and definitions.

## 13 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

**PACKAGING INFORMATION**

| Orderable Device | Status<br>(1) | Package Type | Package<br>Drawing | Pins | Package<br>Qty | Eco Plan<br>(2)            | Lead/Ball Finish<br>(6) | MSL Peak Temp<br>(3) | Op Temp (°C) | Device Marking<br>(4/5) | Samples                 |
|------------------|---------------|--------------|--------------------|------|----------------|----------------------------|-------------------------|----------------------|--------------|-------------------------|-------------------------|
| LM431ACM         | NRND          | SOIC         | D                  | 8    | 95             | TBD                        | Call TI                 | Call TI              | 0 to 70      | LM431<br>ACM            |                         |
| LM431ACM/NOPB    | ACTIVE        | SOIC         | D                  | 8    | 95             | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | 0 to 70      | LM431<br>ACM            | <a href="#">Samples</a> |
| LM431ACM3/NOPB   | ACTIVE        | SOT-23       | DBZ                | 3    | 1000           | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | 0 to 70      | N1F                     | <a href="#">Samples</a> |
| LM431ACM3X       | NRND          | SOT-23       | DBZ                | 3    | 3000           | TBD                        | Call TI                 | Call TI              | 0 to 70      | N1F                     |                         |
| LM431ACM3X/NOPB  | ACTIVE        | SOT-23       | DBZ                | 3    | 3000           | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | 0 to 70      | N1F                     | <a href="#">Samples</a> |
| LM431ACMX/NOPB   | ACTIVE        | SOIC         | D                  | 8    | 2500           | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | 0 to 70      | LM431<br>ACM            | <a href="#">Samples</a> |
| LM431ACZ/LFT3    | ACTIVE        | TO-92        | LP                 | 3    | 2000           | Green (RoHS<br>& no Sb/Br) | CU SN                   | N / A for Pkg Type   |              | LM431<br>ACZ            | <a href="#">Samples</a> |
| LM431ACZ/LFT4    | ACTIVE        | TO-92        | LP                 | 3    | 2000           | Green (RoHS<br>& no Sb/Br) | CU SN                   | N / A for Pkg Type   |              | LM431<br>ACZ            | <a href="#">Samples</a> |
| LM431ACZ/NOPB    | ACTIVE        | TO-92        | LP                 | 3    | 1800           | Green (RoHS<br>& no Sb/Br) | CU SN                   | N / A for Pkg Type   | 0 to 70      | LM431<br>ACZ            | <a href="#">Samples</a> |
| LM431AIM         | NRND          | SOIC         | D                  | 8    | 95             | TBD                        | Call TI                 | Call TI              | -40 to 85    | LM431<br>AIM            |                         |
| LM431AIM/NOPB    | ACTIVE        | SOIC         | D                  | 8    | 95             | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | -40 to 85    | LM431<br>AIM            | <a href="#">Samples</a> |
| LM431AIM3        | NRND          | SOT-23       | DBZ                | 3    | 1000           | TBD                        | Call TI                 | Call TI              | -40 to 85    | N1E                     |                         |
| LM431AIM3/NOPB   | ACTIVE        | SOT-23       | DBZ                | 3    | 1000           | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | -40 to 85    | N1E                     | <a href="#">Samples</a> |
| LM431AIM3X/NOPB  | ACTIVE        | SOT-23       | DBZ                | 3    | 3000           | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | -40 to 85    | N1E                     | <a href="#">Samples</a> |
| LM431AIMX/NOPB   | ACTIVE        | SOIC         | D                  | 8    | 2500           | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | -40 to 85    | LM431<br>AIM            | <a href="#">Samples</a> |
| LM431AIZ/LFT1    | ACTIVE        | TO-92        | LP                 | 3    | 2000           | Green (RoHS<br>& no Sb/Br) | CU SN                   | N / A for Pkg Type   |              | LM431<br>AIZ            | <a href="#">Samples</a> |
| LM431AIZ/NOPB    | ACTIVE        | TO-92        | LP                 | 3    | 1800           | Green (RoHS<br>& no Sb/Br) | CU SN                   | N / A for Pkg Type   | -40 to 85    | LM431<br>AIZ            | <a href="#">Samples</a> |
| LM431BCM/NOPB    | ACTIVE        | SOIC         | D                  | 8    | 95             | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | 0 to 70      | 431<br>BCM              | <a href="#">Samples</a> |

| Orderable Device | Status<br>(1) | Package Type | Package<br>Drawing | Pins | Package<br>Qty | Eco Plan<br>(2)            | Lead/Ball Finish<br>(6) | MSL Peak Temp<br>(3) | Op Temp (°C) | Device Marking<br>(4/5) | Samples                 |
|------------------|---------------|--------------|--------------------|------|----------------|----------------------------|-------------------------|----------------------|--------------|-------------------------|-------------------------|
| LM431BCM3        | NRND          | SOT-23       | DBZ                | 3    | 1000           | TBD                        | Call TI                 | Call TI              | 0 to 70      | N1D                     |                         |
| LM431BCM3/NOPB   | ACTIVE        | SOT-23       | DBZ                | 3    | 1000           | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | 0 to 70      | N1D                     | <a href="#">Samples</a> |
| LM431BCM3X/NOPB  | ACTIVE        | SOT-23       | DBZ                | 3    | 3000           | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | 0 to 70      | N1D                     | <a href="#">Samples</a> |
| LM431BCMX/NOPB   | ACTIVE        | SOIC         | D                  | 8    | 2500           | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | 0 to 70      | 431<br>BCM              | <a href="#">Samples</a> |
| LM431BCZ/NOPB    | ACTIVE        | TO-92        | LP                 | 3    | 1800           | Green (RoHS<br>& no Sb/Br) | CU SN                   | N / A for Pkg Type   | 0 to 70      | LM431<br>BCZ            | <a href="#">Samples</a> |
| LM431BIM         | NRND          | SOIC         | D                  | 8    | 95             | TBD                        | Call TI                 | Call TI              | -40 to 85    | 431<br>BIM              |                         |
| LM431BIM/NOPB    | ACTIVE        | SOIC         | D                  | 8    | 95             | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | -40 to 85    | 431<br>BIM              | <a href="#">Samples</a> |
| LM431BIM3        | NRND          | SOT-23       | DBZ                | 3    | 1000           | TBD                        | Call TI                 | Call TI              | -40 to 85    | N1C                     |                         |
| LM431BIM3/NOPB   | ACTIVE        | SOT-23       | DBZ                | 3    | 1000           | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | -40 to 85    | N1C                     | <a href="#">Samples</a> |
| LM431BIM3X       | NRND          | SOT-23       | DBZ                | 3    | 3000           | TBD                        | Call TI                 | Call TI              | -40 to 85    | N1C                     |                         |
| LM431BIM3X/NOPB  | ACTIVE        | SOT-23       | DBZ                | 3    | 3000           | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | -40 to 85    | N1C                     | <a href="#">Samples</a> |
| LM431BIMX/NOPB   | ACTIVE        | SOIC         | D                  | 8    | 2500           | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | -40 to 85    | 431<br>BIM              | <a href="#">Samples</a> |
| LM431CCM/NOPB    | ACTIVE        | SOIC         | D                  | 8    | 95             | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | 0 to 70      | 431<br>CCM              | <a href="#">Samples</a> |
| LM431CCM3/NOPB   | ACTIVE        | SOT-23       | DBZ                | 3    | 1000           | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | 0 to 70      | N1B                     | <a href="#">Samples</a> |
| LM431CCM3X       | NRND          | SOT-23       | DBZ                | 3    | 3000           | TBD                        | Call TI                 | Call TI              | 0 to 70      | N1B                     |                         |
| LM431CCM3X/NOPB  | ACTIVE        | SOT-23       | DBZ                | 3    | 3000           | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | 0 to 70      | N1B                     | <a href="#">Samples</a> |
| LM431CCZ/NOPB    | ACTIVE        | TO-92        | LP                 | 3    | 1800           | Green (RoHS<br>& no Sb/Br) | CU SN                   | N / A for Pkg Type   | 0 to 70      | LM431<br>CCZ            | <a href="#">Samples</a> |
| LM431CIM         | NRND          | SOIC         | D                  | 8    | 95             | TBD                        | Call TI                 | Call TI              | -40 to 85    | 431<br>CIM              |                         |
| LM431CIM/NOPB    | ACTIVE        | SOIC         | D                  | 8    | 95             | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | -40 to 85    | 431<br>CIM              | <a href="#">Samples</a> |
| LM431CIM3        | NRND          | SOT-23       | DBZ                | 3    | 1000           | TBD                        | Call TI                 | Call TI              | -40 to 85    | N1A                     |                         |

| Orderable Device | Status<br>(1) | Package Type | Package<br>Drawing | Pins | Package<br>Qty | Eco Plan<br>(2)            | Lead/Ball Finish<br>(6) | MSL Peak Temp<br>(3) | Op Temp (°C) | Device Marking<br>(4/5) | Samples                 |
|------------------|---------------|--------------|--------------------|------|----------------|----------------------------|-------------------------|----------------------|--------------|-------------------------|-------------------------|
| LM431CIM3/NOPB   | ACTIVE        | SOT-23       | DBZ                | 3    | 1000           | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | -40 to 85    | N1A                     | <a href="#">Samples</a> |
| LM431CIM3X       | NRND          | SOT-23       | DBZ                | 3    | 3000           | TBD                        | Call TI                 | Call TI              | -40 to 85    | N1A                     |                         |
| LM431CIM3X/NOPB  | ACTIVE        | SOT-23       | DBZ                | 3    | 3000           | Green (RoHS<br>& no Sb/Br) | CU SN                   | Level-1-260C-UNLIM   | -40 to 85    | N1A                     | <a href="#">Samples</a> |
| LM431CIZ/NOPB    | ACTIVE        | TO-92        | LP                 | 3    | 1800           | Green (RoHS<br>& no Sb/Br) | CU SN                   | N / A for Pkg Type   | -40 to 85    | LM431<br>CIZ            | <a href="#">Samples</a> |

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

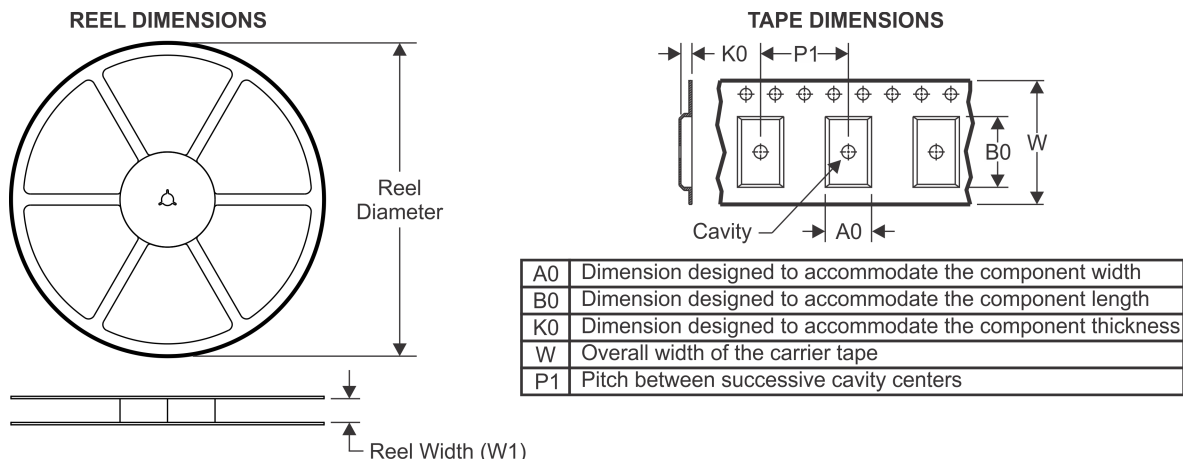
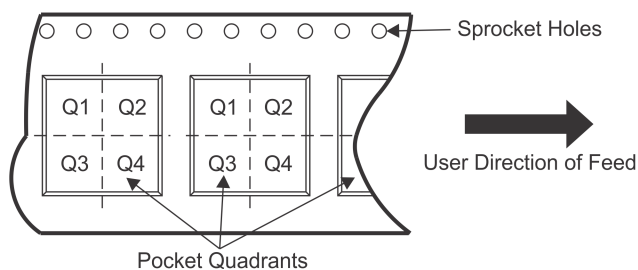
(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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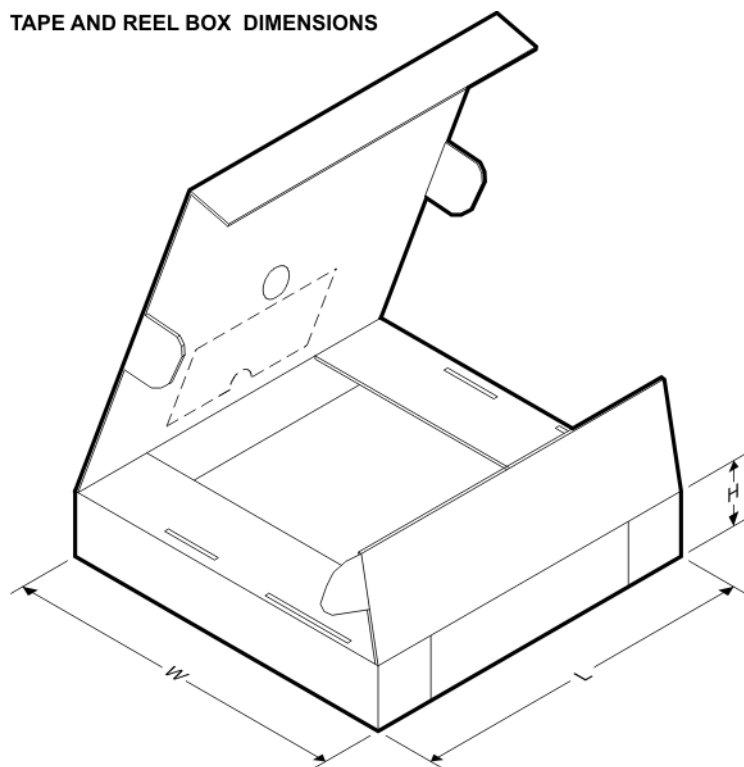
**TAPE AND REEL INFORMATION**

**QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE**


\*All dimensions are nominal

| Device          | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|-----------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| LM431ACM3/NOPB  | SOT-23       | DBZ             | 3    | 1000 | 178.0              | 8.4                | 3.3     | 2.9     | 1.22    | 4.0     | 8.0    | Q3            |
| LM431ACM3X      | SOT-23       | DBZ             | 3    | 3000 | 178.0              | 8.4                | 3.3     | 2.9     | 1.22    | 4.0     | 8.0    | Q3            |
| LM431ACM3X/NOPB | SOT-23       | DBZ             | 3    | 3000 | 178.0              | 8.4                | 3.3     | 2.9     | 1.22    | 4.0     | 8.0    | Q3            |
| LM431ACMX/NOPB  | SOIC         | D               | 8    | 2500 | 330.0              | 12.4               | 6.5     | 5.4     | 2.0     | 8.0     | 12.0   | Q1            |
| LM431AIM3       | SOT-23       | DBZ             | 3    | 1000 | 178.0              | 8.4                | 3.3     | 2.9     | 1.22    | 4.0     | 8.0    | Q3            |
| LM431AIM3/NOPB  | SOT-23       | DBZ             | 3    | 1000 | 178.0              | 8.4                | 3.3     | 2.9     | 1.22    | 4.0     | 8.0    | Q3            |
| LM431AIM3X/NOPB | SOT-23       | DBZ             | 3    | 3000 | 178.0              | 8.4                | 3.3     | 2.9     | 1.22    | 4.0     | 8.0    | Q3            |
| LM431AIMX/NOPB  | SOIC         | D               | 8    | 2500 | 330.0              | 12.4               | 6.5     | 5.4     | 2.0     | 8.0     | 12.0   | Q1            |
| LM431BCM3       | SOT-23       | DBZ             | 3    | 1000 | 178.0              | 8.4                | 3.3     | 2.9     | 1.22    | 4.0     | 8.0    | Q3            |
| LM431BCM3/NOPB  | SOT-23       | DBZ             | 3    | 1000 | 178.0              | 8.4                | 3.3     | 2.9     | 1.22    | 4.0     | 8.0    | Q3            |
| LM431BCM3X/NOPB | SOT-23       | DBZ             | 3    | 3000 | 178.0              | 8.4                | 3.3     | 2.9     | 1.22    | 4.0     | 8.0    | Q3            |
| LM431BCMX/NOPB  | SOIC         | D               | 8    | 2500 | 330.0              | 12.4               | 6.5     | 5.4     | 2.0     | 8.0     | 12.0   | Q1            |
| LM431BIM3       | SOT-23       | DBZ             | 3    | 1000 | 178.0              | 8.4                | 3.3     | 2.9     | 1.22    | 4.0     | 8.0    | Q3            |
| LM431BIM3/NOPB  | SOT-23       | DBZ             | 3    | 1000 | 178.0              | 8.4                | 3.3     | 2.9     | 1.22    | 4.0     | 8.0    | Q3            |
| LM431BIM3X      | SOT-23       | DBZ             | 3    | 3000 | 178.0              | 8.4                | 3.3     | 2.9     | 1.22    | 4.0     | 8.0    | Q3            |
| LM431BIM3X/NOPB | SOT-23       | DBZ             | 3    | 3000 | 178.0              | 8.4                | 3.3     | 2.9     | 1.22    | 4.0     | 8.0    | Q3            |
| LM431BIMX/NOPB  | SOIC         | D               | 8    | 2500 | 330.0              | 12.4               | 6.5     | 5.4     | 2.0     | 8.0     | 12.0   | Q1            |
| LM431CCM3/NOPB  | SOT-23       | DBZ             | 3    | 1000 | 178.0              | 8.4                | 3.3     | 2.9     | 1.22    | 4.0     | 8.0    | Q3            |

| Device          | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|-----------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| LM431CCM3X      | SOT-23       | DBZ             | 3    | 3000 | 178.0              | 8.4                | 3.3     | 2.9     | 1.22    | 4.0     | 8.0    | Q3            |
| LM431CCM3X/NOPB | SOT-23       | DBZ             | 3    | 3000 | 178.0              | 8.4                | 3.3     | 2.9     | 1.22    | 4.0     | 8.0    | Q3            |
| LM431CIM3       | SOT-23       | DBZ             | 3    | 1000 | 178.0              | 8.4                | 3.3     | 2.9     | 1.22    | 4.0     | 8.0    | Q3            |
| LM431CIM3/NOPB  | SOT-23       | DBZ             | 3    | 1000 | 178.0              | 8.4                | 3.3     | 2.9     | 1.22    | 4.0     | 8.0    | Q3            |
| LM431CIM3X      | SOT-23       | DBZ             | 3    | 3000 | 178.0              | 8.4                | 3.3     | 2.9     | 1.22    | 4.0     | 8.0    | Q3            |
| LM431CIM3X/NOPB | SOT-23       | DBZ             | 3    | 3000 | 178.0              | 8.4                | 3.3     | 2.9     | 1.22    | 4.0     | 8.0    | Q3            |

## TAPE AND REEL BOX DIMENSIONS



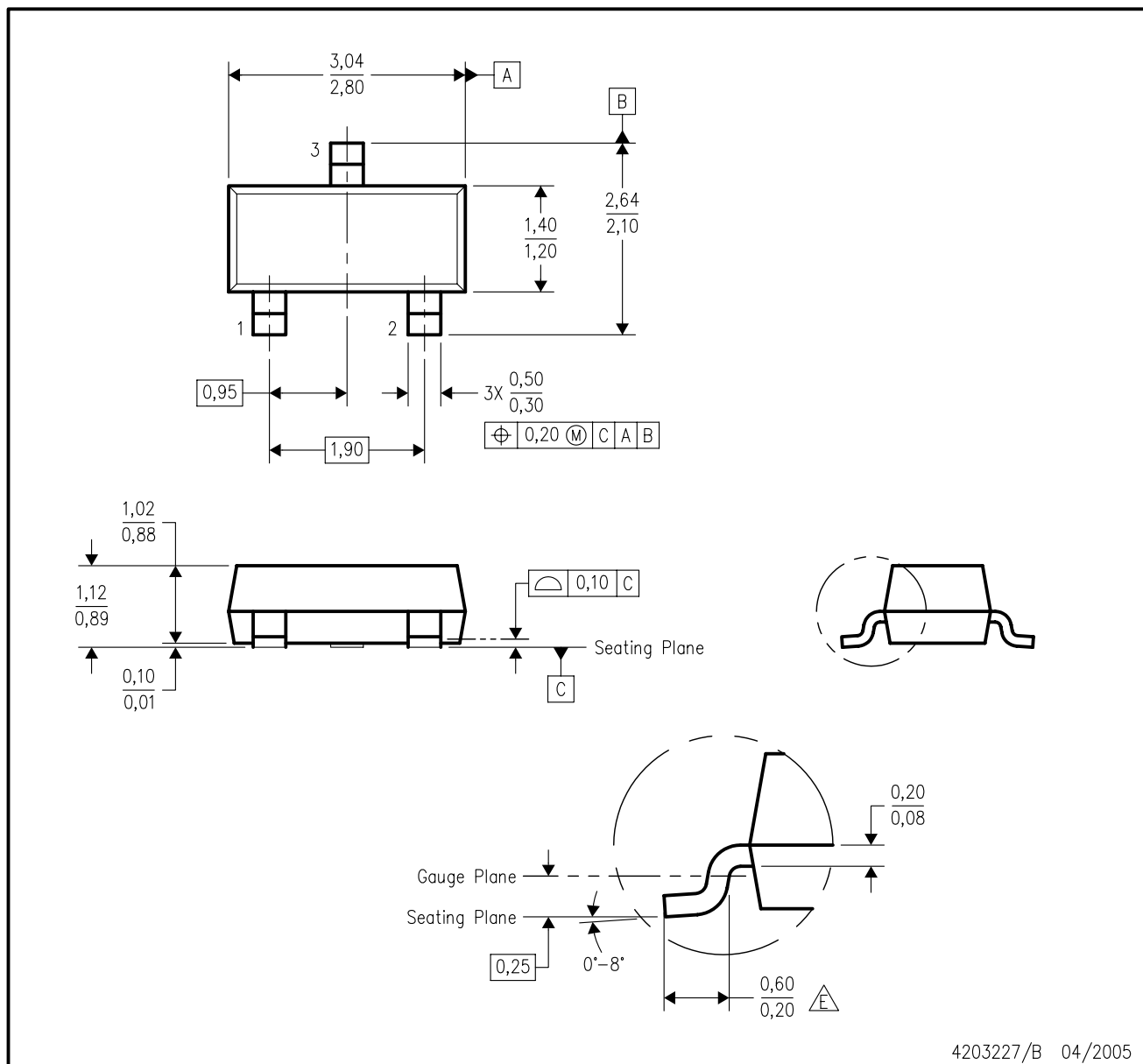
\*All dimensions are nominal

| Device          | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|-----------------|--------------|-----------------|------|------|-------------|------------|-------------|
| LM431ACM3/NOPB  | SOT-23       | DBZ             | 3    | 1000 | 210.0       | 185.0      | 35.0        |
| LM431ACM3X      | SOT-23       | DBZ             | 3    | 3000 | 210.0       | 185.0      | 35.0        |
| LM431ACM3X/NOPB | SOT-23       | DBZ             | 3    | 3000 | 210.0       | 185.0      | 35.0        |
| LM431ACMX/NOPB  | SOIC         | D               | 8    | 2500 | 367.0       | 367.0      | 35.0        |
| LM431AIM3       | SOT-23       | DBZ             | 3    | 1000 | 210.0       | 185.0      | 35.0        |
| LM431AIM3/NOPB  | SOT-23       | DBZ             | 3    | 1000 | 210.0       | 185.0      | 35.0        |
| LM431AIM3X/NOPB | SOT-23       | DBZ             | 3    | 3000 | 210.0       | 185.0      | 35.0        |
| LM431AIMX/NOPB  | SOIC         | D               | 8    | 2500 | 367.0       | 367.0      | 35.0        |
| LM431BCM3       | SOT-23       | DBZ             | 3    | 1000 | 210.0       | 185.0      | 35.0        |
| LM431BCM3/NOPB  | SOT-23       | DBZ             | 3    | 1000 | 210.0       | 185.0      | 35.0        |
| LM431BCM3X/NOPB | SOT-23       | DBZ             | 3    | 3000 | 210.0       | 185.0      | 35.0        |

| Device          | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|-----------------|--------------|-----------------|------|------|-------------|------------|-------------|
| LM431BCMX/NOPB  | SOIC         | D               | 8    | 2500 | 367.0       | 367.0      | 35.0        |
| LM431BIM3       | SOT-23       | DBZ             | 3    | 1000 | 210.0       | 185.0      | 35.0        |
| LM431BIM3/NOPB  | SOT-23       | DBZ             | 3    | 1000 | 210.0       | 185.0      | 35.0        |
| LM431BIM3X      | SOT-23       | DBZ             | 3    | 3000 | 210.0       | 185.0      | 35.0        |
| LM431BIM3X/NOPB | SOT-23       | DBZ             | 3    | 3000 | 210.0       | 185.0      | 35.0        |
| LM431BIMX/NOPB  | SOIC         | D               | 8    | 2500 | 367.0       | 367.0      | 35.0        |
| LM431CCM3/NOPB  | SOT-23       | DBZ             | 3    | 1000 | 210.0       | 185.0      | 35.0        |
| LM431CCM3X      | SOT-23       | DBZ             | 3    | 3000 | 210.0       | 185.0      | 35.0        |
| LM431CCM3X/NOPB | SOT-23       | DBZ             | 3    | 3000 | 210.0       | 185.0      | 35.0        |
| LM431CIM3       | SOT-23       | DBZ             | 3    | 1000 | 210.0       | 185.0      | 35.0        |
| LM431CIM3/NOPB  | SOT-23       | DBZ             | 3    | 1000 | 210.0       | 185.0      | 35.0        |
| LM431CIM3X      | SOT-23       | DBZ             | 3    | 3000 | 210.0       | 185.0      | 35.0        |
| LM431CIM3X/NOPB | SOT-23       | DBZ             | 3    | 3000 | 210.0       | 185.0      | 35.0        |

DBZ (R-PDSO-G3)

PLASTIC SMALL-OUTLINE



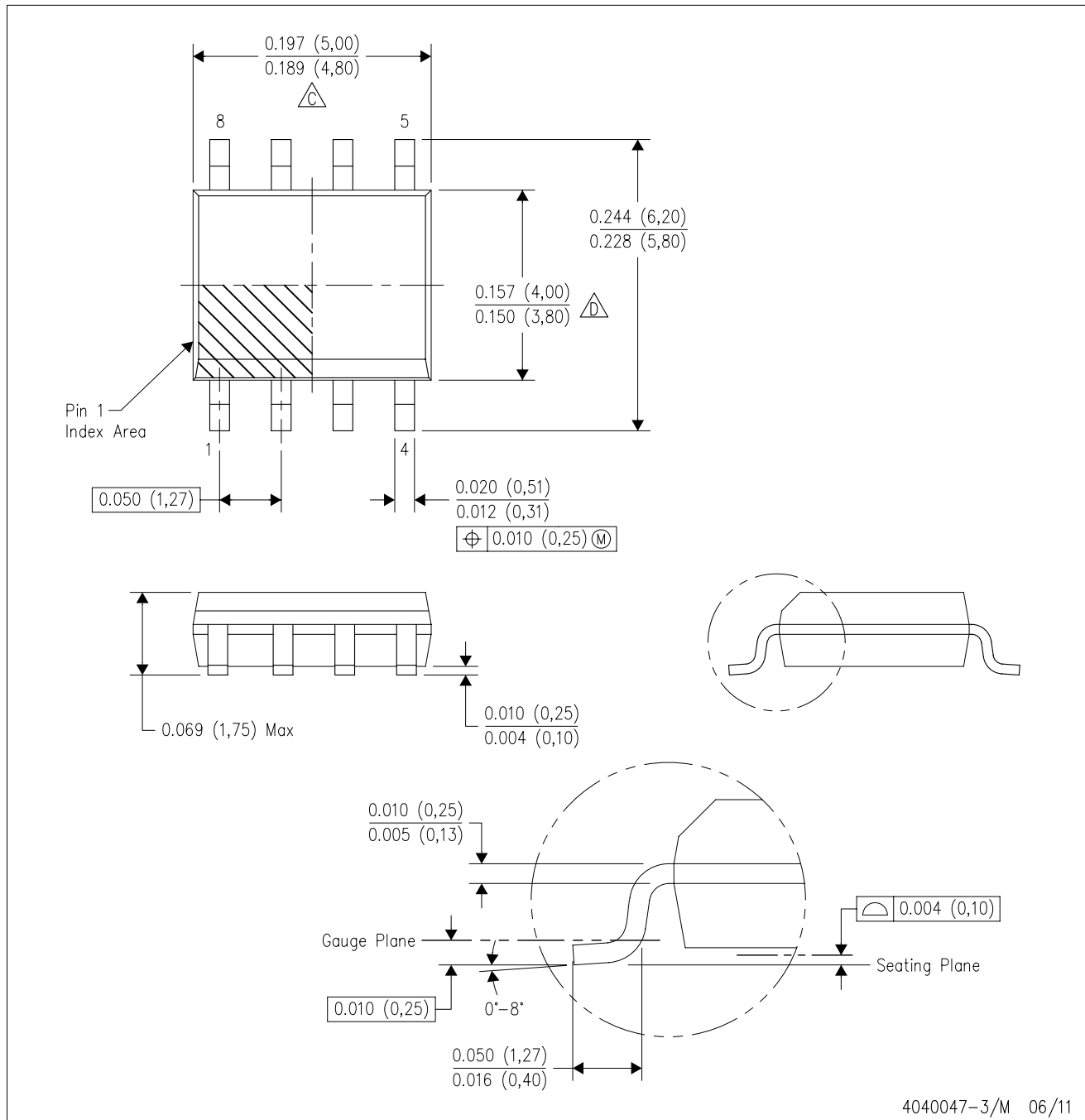
4203227/B 04/2005

NOTES:

- All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M-1994.
- This drawing is subject to change without notice.
- Lead dimensions are inclusive of plating.
- Body dimensions are exclusive of mold flash and protrusion. Mold flash and protrusion not to exceed 0.25 per side.
- Falls within JEDEC TO-236 variation AB, except minimum foot length.

D (R-PDSO-G8)

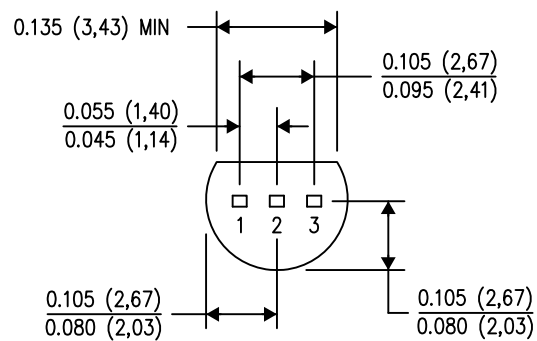
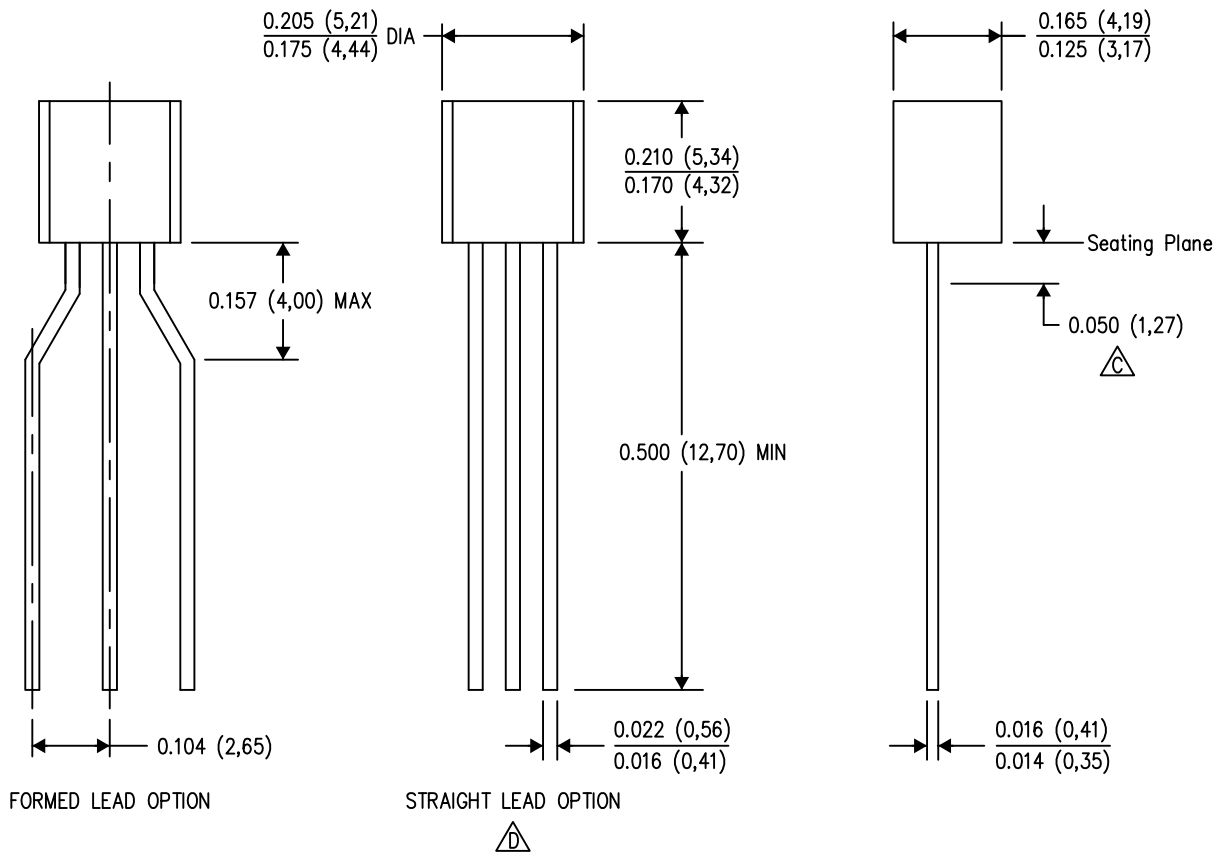
PLASTIC SMALL OUTLINE



- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - $\triangle C$  Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
  - $\triangle D$  Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
  - E. Reference JEDEC MS-012 variation AA.

LP (O-PBCY-W3)

PLASTIC CYLINDRICAL PACKAGE

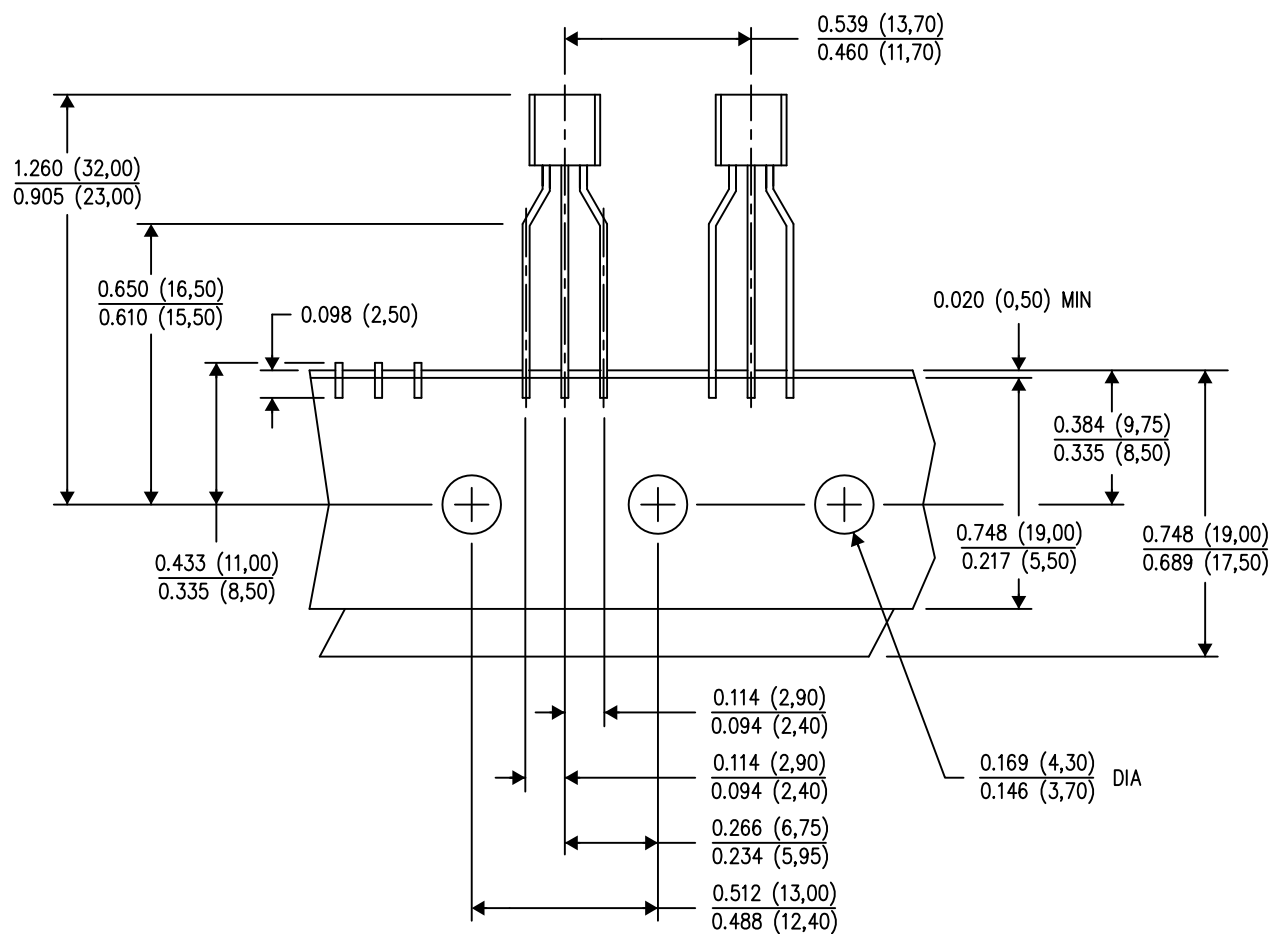


4040001-2/E 08/13

- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - C. Lead dimensions are not controlled within this area.
  - D. Falls within JEDEC TO-226 Variation AA (TO-226 replaces TO-92).
  - E. Shipping Method:  
 Straight lead option available in bulk pack only.  
 Formed lead option available in tape & reel or ammo pack.  
 Specific products can be offered in limited combinations of shipping mediums and lead options.  
 Consult product folder for more information on available options.

LP (O-PBCY-W3)

PLASTIC CYLINDRICAL PACKAGE



TAPE &amp; REEL

4040001-3/E 08/13

- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - C. Tape and Reel information for the Formed Lead Option package.

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|                              |                                                                                      |
|------------------------------|--------------------------------------------------------------------------------------|
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| Data Converters              | <a href="http://dataconverter.ti.com">dataconverter.ti.com</a>                       |
| DLP® Products                | <a href="http://www.dlp.com">www.dlp.com</a>                                         |
| DSP                          | <a href="http://dsp.ti.com">dsp.ti.com</a>                                           |
| Clocks and Timers            | <a href="http://www.ti.com/clocks">www.ti.com/clocks</a>                             |
| Interface                    | <a href="http://interface.ti.com">interface.ti.com</a>                               |
| Logic                        | <a href="http://logic.ti.com">logic.ti.com</a>                                       |
| Power Mgmt                   | <a href="http://power.ti.com">power.ti.com</a>                                       |
| Microcontrollers             | <a href="http://microcontroller.ti.com">microcontroller.ti.com</a>                   |
| RFID                         | <a href="http://www.ti-rfid.com">www.ti-rfid.com</a>                                 |
| OMAP Applications Processors | <a href="http://www.ti.com/omap">www.ti.com/omap</a>                                 |
| Wireless Connectivity        | <a href="http://www.ti.com/wirelessconnectivity">www.ti.com/wirelessconnectivity</a> |

### Applications

|                               |                                                                                          |
|-------------------------------|------------------------------------------------------------------------------------------|
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| Communications and Telecom    | <a href="http://www.ti.com/communications">www.ti.com/communications</a>                 |
| Computers and Peripherals     | <a href="http://www.ti.com/computers">www.ti.com/computers</a>                           |
| Consumer Electronics          | <a href="http://www.ti.com/consumer-apps">www.ti.com/consumer-apps</a>                   |
| Energy and Lighting           | <a href="http://www.ti.com/energy">www.ti.com/energy</a>                                 |
| Industrial                    | <a href="http://www.ti.com/industrial">www.ti.com/industrial</a>                         |
| Medical                       | <a href="http://www.ti.com/medical">www.ti.com/medical</a>                               |
| Security                      | <a href="http://www.ti.com/security">www.ti.com/security</a>                             |
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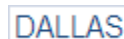
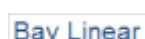
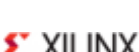
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