

The dual processor architecture of the CX24951 allows it to be used in CableHome applications, without needing an additional network processor for the firewall and routing features required by the CableHome standard. The firewall and routing algorithms required by CableHome take less than 10 percent of the 500 MIPS processing power of the CX24951, leaving plenty of processing power left for developing custom networking applications.

The integrated MII port and the PCMCIA functionality of the integrated host port allow the CX24951 to provide a seamless interface to an array of networking peripherals such as wireless LANs (802.11a/b), HomePlug transceivers, HPNA solutions, and multi-port Ethernet Switches.

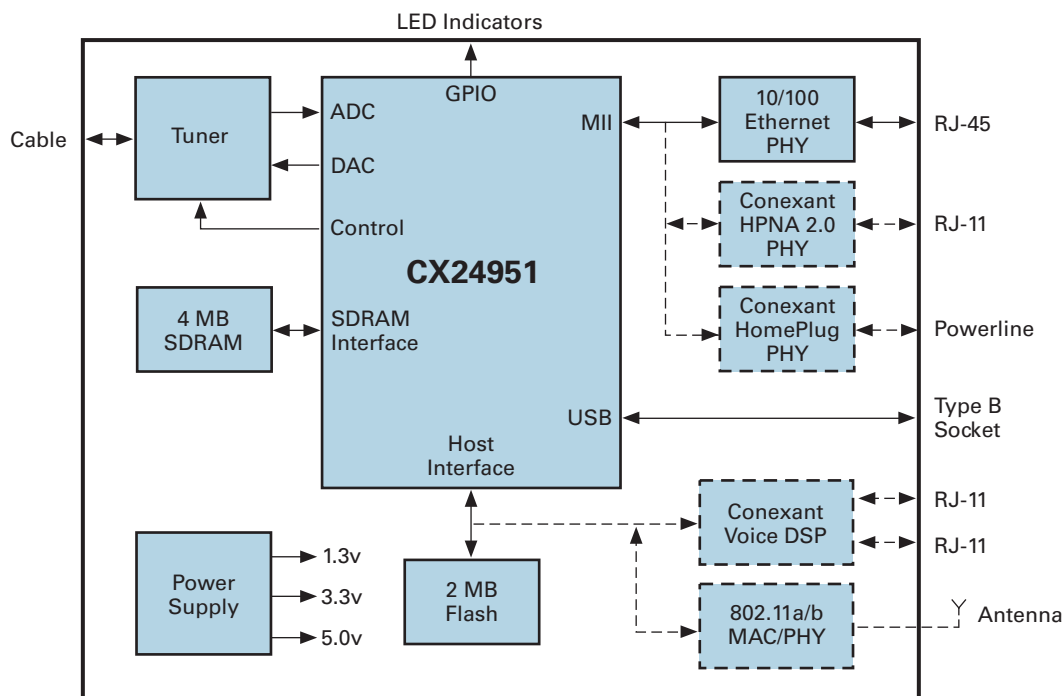
The CX24951 also supports PacketCable applications by interfacing to Conexant's voice DSP products to execute voice processing algorithms.

Highly Integrated

The CX24951 supports a number of interfaces including a USB 1.1 communications transceiver, an IEEE 802.3 10/100 Ethernet Media Access Controller (MAC) with a Media Independent Interface (MII) for access to home networking standards. Examples are HPNA 2.0, HomePlug 1.0 and a host interface which supports seamless connectivity to any PCMCIA compliance device such as IEEE 802.11a and IEEE 802.11b wireless transceivers. The CX24951 supports integrated and stand alone cable modems, home networking gateway products, IP (Internet Protocol) telephony products, and digital set-top boxes.

The CX24951 was developed using leading-edge process and packaging technologies. This helps drive down the size of the monolithic IC, package and power, while allowing clock rates to increase. The CX24951 dissipates less than 550mW of power setting a new standard for the industry.

The CX24951, through its high level of integration, enables manufacturers to develop a complete cable modem with a minimal number of additional components as shown in the CX24951-based cable modem block diagram.



CX24951-based Cable Modem Block Diagram

Increased Performance

Conexant's seventh-generation physical-layer downstream receiver includes a 10-bit A/D that accepts a direct IF signal (at 44 or 36 MHz) and a QPSK/16/32/64/128/256 QAM demodulator with Annex A, B and C forward error correction. The CX24951 also contains an 11-bit D/A converter, which can accommodate 65 MHz of upstream bandwidth for EuroDOCSIS and DVB applications, and a QPSK/8/16/32/64/128 QAM burst upstream modulator. Conexant's proven PHY technology exceeds all DOCSIS/EuroDOCSIS 2.0/1.1/1.0 requirements for low BER in noise-filled environments.

The CX24951 is the fastest solution in the industry with integrated dual software programmable network processing engines on board. Its unique packet management, unified memory architecture, and advanced system buses, maximize performance by allowing segmentation of real-time-critical DOCSIS/EuroDOCSIS 2.0/1.1/1.0 functions from non real-time-critical application processes. The CX24951 processor performance and unique architecture enables DOCSIS/EuroDOCSIS 2.0/1.1/1.0 and other software applications, including VPN firewall for added security and routing for cable home or VoIP for PacketCable.

The CX24951 also incorporates a PHS (Payload Header Suppression requirement introduced in DOCSIS 1.1) engine in hardware. Implementation of PHS in hardware further boosts the performance of the CX24951 by relieving the internal CPU from performing repeated filtering operations.

CX24951 Features

Flexible Platform

Cable modem manufacturers have to devote a significant amount of resources to bring new certifiable solutions to market. The unique architecture of the CX24951 provides a number of advantages for cable modem manufacturers and cable operators alike. The software programmability of the dual network processing engine allows manufacturers to customize the CX24951 for application specific requirements, both at the cable MAC and software applications layer.

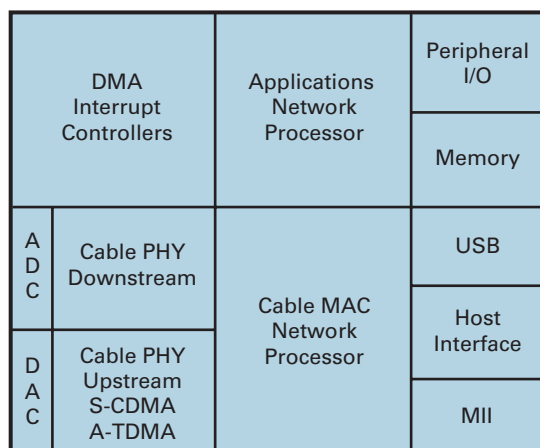
The software programmability of the CX24951 allows one base platform to support numerous products, including PacketCable, CableHome and set-top box products as shown in the CX24951-based cable modem block diagram. In addition, the architectural similarities between the CX24951 and its predecessor, the CX24943, allows for maximum reuse of certified code.

The CX24951 provides the ability to adapt to a multitude of standards by downloading different software into the embedded dual network processing engine, including DOCSIS/EuroDOCSIS 2.0/1.1/1.0, DVB and proprietary standards. Finally, cable operators can download complex feature enhancements above and beyond simple code upgrades to field-deployed solutions, minimizing truck rolls and equipment obsolescence.

Complete Hardware and Software Solution

To reduce the resources and development time required to bring CX24951-based cable modem solutions to market, Conexant provides complete certifiable DOCSIS/EuroDOCSIS 2.0/1.1/1.0 cable modem reference designs which incorporate all of the hardware and software needed for DOCSIS certification.

Cable modem reference designs include Conexant software that is compliant to DOCSIS/EuroDOCSIS 2.0/1.1/1.0 and include the USB drivers that meet Microsoft's Windows Hardware Quality Lab (WHQL) requirements. Additional software enhancements that are above and beyond standards-based requirements are also included in this complete software offering.



**CX24951 Single-Chip
Cable Modem IC Block Diagram**



CX24951 Features

Cable PHY Downstream

- 10-bit A/D
- Direct IF sampling at 44 and 36 MHz
- QPSK/16/32/64/128/256 QAM demodulator
- Programmable equalization
- Annex A, B and C FEC

Cable PHY Upstream

- 11-bit D/A
- A-TDMA
- S-CDMA with Turbo Trellis coding
- QPSK/8/16/32/64/128 QAM burst modulator
- Programmable pre-equalization

DMA Interrupt Controller

- 20 asynchronous channels
- Memory to memory moves: aligned/unaligned
- Source/destination mode for simple data transfer
- Circular/linked buffer support for inbound/outbound data

Peripheral I/O

- 32 GPIO
- JTAG boundary scan
- UART

Cable MAC Network Processor

- Software Programmable
 - DOCSIS 2.0/1.1/1.0 compliant
 - EuroDOCSIS 2.0/1.1/1.0 compliant
 - DVB compliant
 - Fixed wireless compliant
 - Proprietary standards
- Two PHS engines
- Internal 64KB data memory
- Internal 128KB Instruction memory
- Three 56-bit DES/CRC engines
- PID, SID, DA/SA, MAC, IP and LLC filtering

Application Network Processor

- ARM940T microcontroller
- Up to 225 MHz operation
- 4KB instruction and 4KB data cache
- Embedded ICE and debug extensions
- HCS 16- and 32-bit CRC single-cycle engines

MII

- IEEE 802.3 MAC layer
- Networking technologies
 - 10/100 Ethernet
 - Conexant HPNA 2.0
 - Conexant HomePlug 1.0

Host Interface

- Master mode
 - 16-bit data
 - 8-chip selects
- Supports Flash, IP telephony, PCMCIA
 - IEEE 802.11a or IEEE 802.11b
 - 16/32-bit Slave mode (external host and test)

USB 1.1

- Integrated 12 Mb transceiver
- Communications peripheral device

Memory

- Internal 8K x 32 SRAM
- External SDRAM Interface
 - 16-bit interface
 - Supports both 4MB and 8MB technologies

Phase-Locked Loops (4)

- TDM clock and frame sync generator
- Transmit clock generator
- Supplied 25MHz Ethernet reference clock
- Internal USB clock

Miscellaneous Functions

- Watchdog timer
- Four additional timers
- Interrupt controller

Package and Power

- 228 PBGA (17mm x 17mm)
- All I/O at 3.3V
- Power supply: 3.3V and 1.3V
- <550 mW (typical)

Reference Designs

- DOCSIS 2.0/1.1/1.0
- EuroDOCSIS 2.0/1.1/1.0
- PCI

Product Applications

(DOCSIS/EuroDOCSIS 2.0/1.1/1.0, DVB, Proprietary)

- Cable Modems with Ethernet
- Cable Modems with USB
- Cable Modems with IP Telephony
- PCI Cable Modem Card Internal (CMCI)
- Cable Modem with Home Networking
- PCMCIA Cable Modem
- Digital Set-Top Box with Cable Modem
- CPE Controlled Cable Modem (CCCM)



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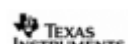
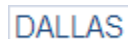
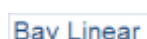
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