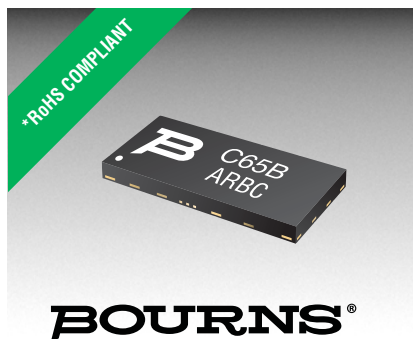




## Features

- Formerly **FulTec** brand
- Extremely high speed performance
- Blocks high voltages and currents
- Very high bandwidth; GHz compatible
- Small package, minimal PCB area
- Simple, superior circuit protection
- RoHS compliant\*, UL Recognized 



The C650 & C850 Series are currently available, but not recommended for new designs. Bourns® **TBU-CA Series** is preferred.

## C650 and C850 Series TBU® High-Speed Protectors

### Transient Blocking Units - TBU® Devices

Bourns® C650 and C850 series products are high-speed bidirectional protection components, constructed using MOSFET semiconductor technology, designed to protect against faults caused by short circuits, AC power cross, induction and lightning surges.

The TBU® high-speed protector, triggering as a function of the MOSFET, blocks surges and provides an effective barrier behind which sensitive electronics are not exposed to large voltages or currents during surge events. The TBU® device is provided in a surface mount DFN package and meets industry standard requirements such as RoHS and Pb Free solder reflow profiles.

### Agency Approval

UL recognized component File # E315805.

### Industry Standards

| Description |                                |                   | Model        |
|-------------|--------------------------------|-------------------|--------------|
| Telcordia   | GR-1089                        | Port Type 1, 3, 5 | C650<br>C850 |
|             | GR-974                         |                   | C650<br>C850 |
| ITU-T       | K.20, K.20E, K.21, K.21E, K.45 |                   | C850         |

### Absolute Maximum Ratings (T<sub>amb</sub> = 25 °C)

| Symbol           | Parameter                                                             |                            | Value       | Unit |
|------------------|-----------------------------------------------------------------------|----------------------------|-------------|------|
| V <sub>imp</sub> | Maximum protection voltage for impulse faults with rise time ≥ 1 μsec | C650-xxx-WH<br>C850-xxx-WH | 650<br>850  | V    |
| V <sub>rms</sub> | Maximum protection voltage for continuous V <sub>rms</sub> faults     | C650-xxx-WH<br>C850-xxx-WH | 300<br>425  | V    |
| T <sub>op</sub>  | Operating temperature range                                           |                            | -40 to +85  | °C   |
| T <sub>stg</sub> | Storage temperature range                                             |                            | -65 to +150 | °C   |

### Electrical Characteristics (T<sub>amb</sub> = 25 °C)

| Symbol                 | Parameter                                                                               |                                                                                        | Min. | Typ.                           | Max.                               | Unit |
|------------------------|-----------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------|------|--------------------------------|------------------------------------|------|
| I <sub>op</sub>        | Maximum current through the device that will not cause current blocking                 | Cx50-100-WH<br>Cx50-180-WH<br>Cx50-260-WH                                              |      |                                | 100<br>180<br>260                  | mA   |
| I <sub>trigger</sub>   | Typical current for the device to go from normal operating state to protected state     | Cx50-100-WH<br>Cx50-180-WH<br>Cx50-260-WH                                              |      | 150<br>220<br>330              |                                    | mA   |
| I <sub>out</sub>       | Maximum current through the device                                                      | Cx50-100-WH<br>Cx50-180-WH<br>Cx50-260-WH                                              |      |                                | 200<br>360<br>520                  | mA   |
| R <sub>device</sub>    | Series resistance of the TBU® device                                                    | C650-100-WH<br>C650-180-WH<br>C650-260-WH<br>C850-100-WH<br>C850-180-WH<br>C850-260-WH |      | 12<br>8<br>8<br>17<br>11<br>11 | 14.5<br>10<br>10<br>19<br>14<br>14 | Ω    |
| t <sub>block</sub>     | Maximum time for the device to go from normal operating state to protected state        |                                                                                        |      |                                | 1                                  | μs   |
| I <sub>quiescent</sub> | Current through the triggered TBU® device with 50 Vdc circuit voltage                   |                                                                                        |      | 1                              |                                    | mA   |
| V <sub>reset</sub>     | Voltage below which the triggered TBU® device will transition to normal operating state |                                                                                        |      | 14                             |                                    | V    |

C650 and C850 TBU® High-Speed Protectors are bidirectional; specifications are valid in both directions.

\*RoHS Directive 2002/95/EC Jan. 27, 2003 including annex and RoHS Recast 2011/65/EU June 8, 2011.

Specifications are subject to change without notice.

Customers should verify actual device performance in their specific applications.

## Applications

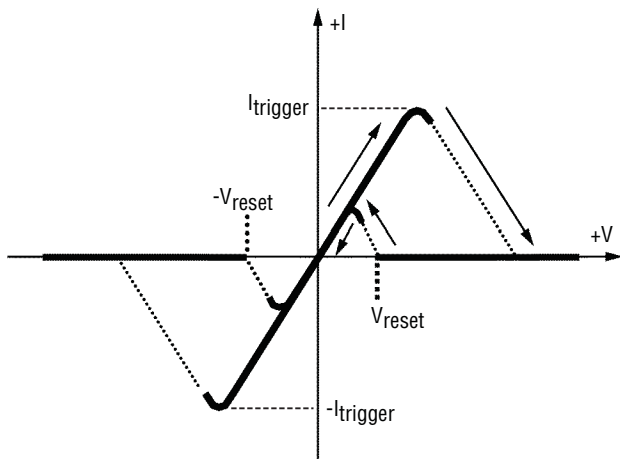
- Combo voice / xDSL linecards
- Voice linecards
- MDF, primary protection modules
- Process control equipment
- Test and measurement equipment
- General electronics

## C650 and C850 Series TBU® High-Speed Protectors

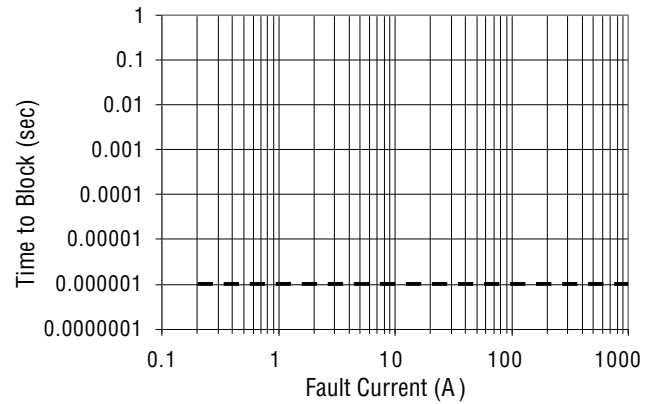
**BOURNS®**

### Typical Performance Characteristics

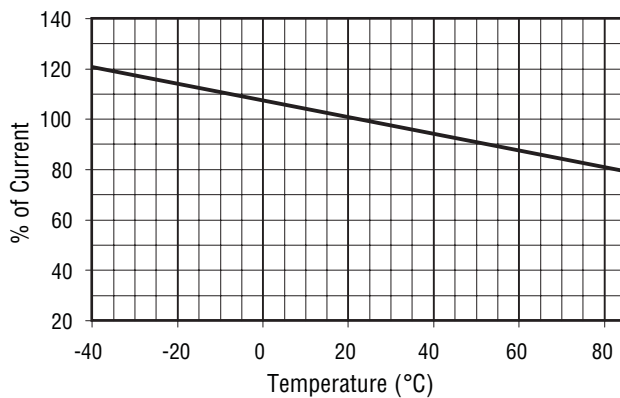
#### V-I Characteristics



#### Time to Block vs. Fault Current



#### Current vs. Temperature



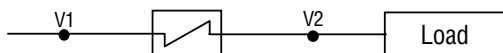
# C650 and C850 Series TBU® High-Speed Protectors

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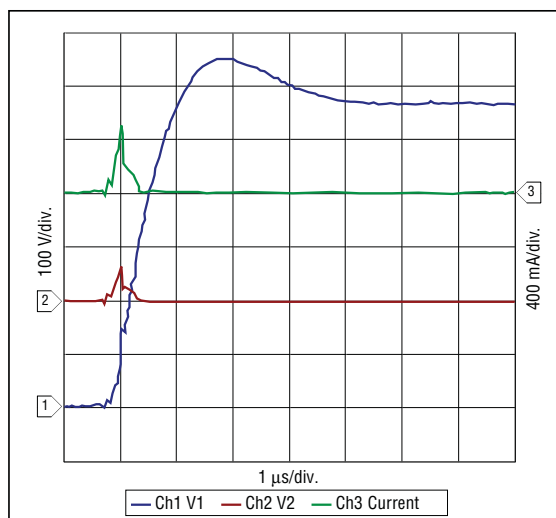
## Operational Characteristics

The graphs below demonstrate the operational characteristics of the TBU® protector. For each graph the fault voltage, protected side voltage, and current is presented.

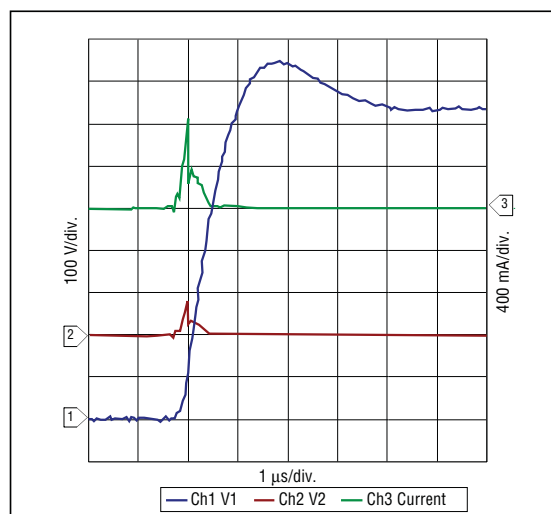
TEST CONFIGURATION DIAGRAM



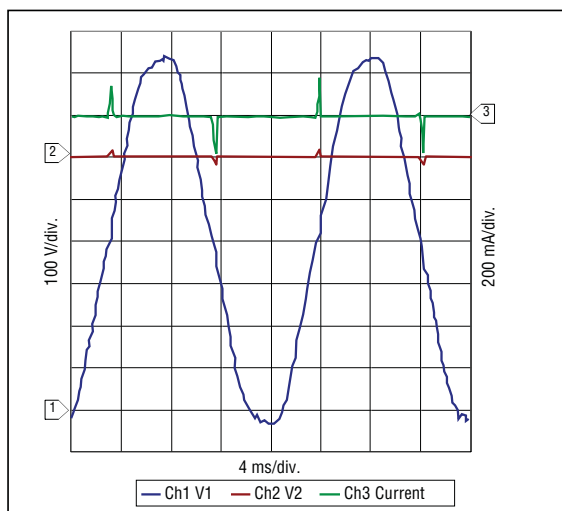
### C650 Lightning, 650 V



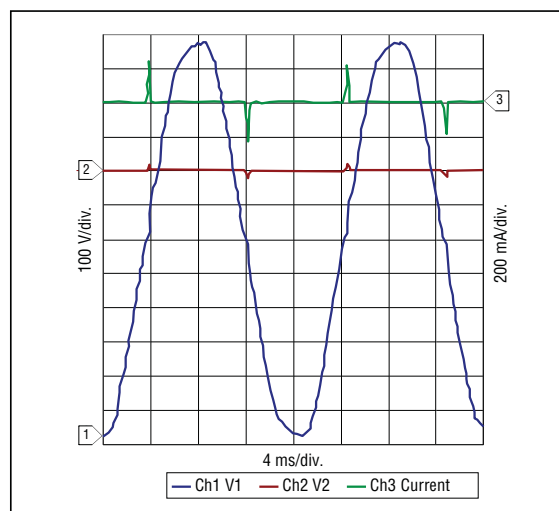
### C850 Lightning, 850 V



### C650 Power Fault, 300 Vrms, 100 A



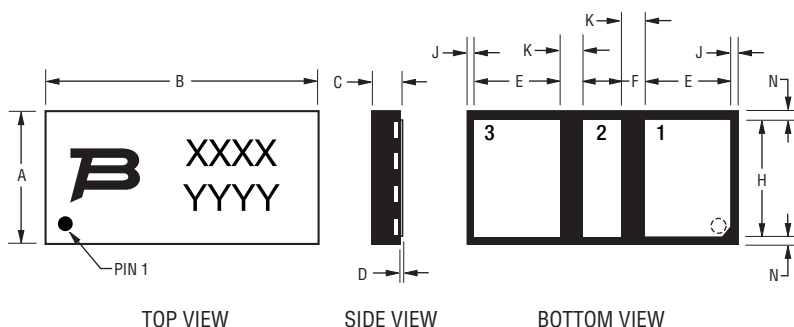
### C850 Power Fault, 425 Vrms, 100 A



# C650 and C850 Series TBU® High-Speed Protectors

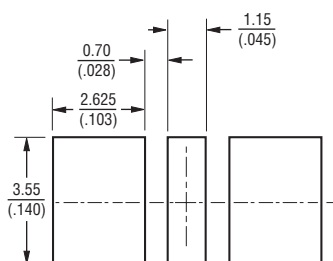
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## Product Dimensions



| Dim. | Min.            | Typ.            | Max.            |
|------|-----------------|-----------------|-----------------|
| A    | 3.90<br>(.154)  | 4.00<br>(.157)  | 4.10<br>(.161)  |
| B    | 8.15<br>(.321)  | 8.25<br>(.325)  | 8.35<br>(.329)  |
| C    | 0.80<br>(.031)  | 0.85<br>(.033)  | 0.90<br>(.035)  |
| D    | 0.000<br>(.000) | 0.025<br>(.001) | 0.050<br>(.002) |
| E    | 2.55<br>(.100)  | 2.60<br>(.102)  | 2.65<br>(.104)  |
| F    | 1.10<br>(.043)  | 1.15<br>(.045)  | 1.20<br>(.047)  |
| H    | 3.45<br>(.136)  | 3.50<br>(.138)  | 3.55<br>(.140)  |
| J    | 0.20<br>(.008)  | 0.25<br>(.010)  | 0.30<br>(.012)  |
| K    | 0.65<br>(.026)  | 0.70<br>(.028)  | 0.75<br>(.030)  |
| N    | 0.20<br>(.008)  | 0.25<br>(.010)  | 0.30<br>(.012)  |

## Recommended Pad Layout



### Pad Designation

| Pad # | Apply  |
|-------|--------|
| 1     | In/Out |
| 2     | NC     |
| 3     | In/Out |

NC = Solder to PCB; do not make electrical connection, do not connect to ground.

DIMENSIONS:  $\frac{\text{MM}}{\text{(INCHES)}}$

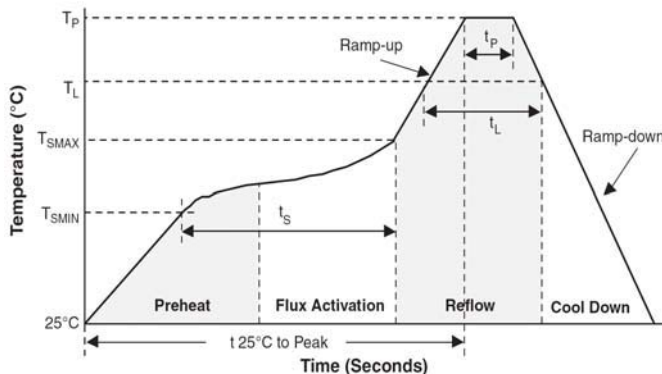
TBU® protectors have matte-tin termination finish. Suggested layout should use non-solder mask define (NSMD). Recommended stencil thickness is 0.10-0.12 mm (.004-.005 in.) with stencil opening size 0.025 mm (.0010 in.) less than the device pad size. As when heat sinking any power device, it is recommended that, wherever possible, extra PCB copper area is allowed. For minimum parasitic capacitance, do not allow any signal, ground or power signals beneath any of the pads of the device.

## Thermal Resistances

| Symbol               | Parameter                   | Value | Unit |
|----------------------|-----------------------------|-------|------|
| R <sub>th(j-a)</sub> | Junction to leads (package) | 116   | °C/W |

## Reflow Profile

| Profile Feature                                             | Pb-Free Assembly |
|-------------------------------------------------------------|------------------|
| Average Ramp-Up Rate (T <sub>smax</sub> to T <sub>p</sub> ) | 3 °C/sec. max.   |
| Preheat                                                     |                  |
| - Temperature Min. (T <sub>smin</sub> )                     | 150 °C           |
| - Temperature Max. (T <sub>smax</sub> )                     | 200 °C           |
| - Time (t <sub>smin</sub> to t <sub>smax</sub> )            | 60-180 sec.      |
| Time maintained above:                                      |                  |
| - Temperature (T <sub>L</sub> )                             | 217 °C           |
| - Time (t <sub>L</sub> )                                    | 60-150 sec.      |
| Peak/Classification Temperature (T <sub>p</sub> )           | 260 °C           |
| Time within 5 °C of Actual Peak Temp. (t <sub>p</sub> )     | 20-40 sec.       |
| Ramp-Down Rate                                              | 6 °C/sec. max.   |
| Time 25 °C to Peak Temperature                              | 8 min. max.      |



Specifications are subject to change without notice.  
Customers should verify actual device performance in their specific applications.

# C650 and C850 Series TBU® High-Speed Protectors

**BOURNS®**

## How to Order

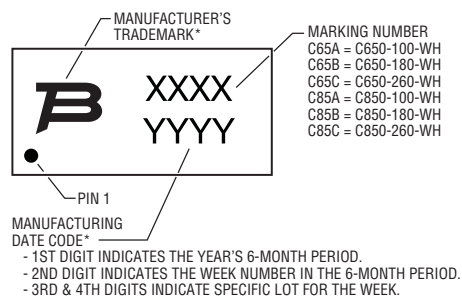
**C 650 - 180 - WH**

Form Factor \_\_\_\_\_  
C = One TBU® protector in the device

Impulse Voltage Rating \_\_\_\_\_  
650 = 650 V  
850 = 850 V

lop Indicator \_\_\_\_\_  
100 = 100 mA  
180 = 180 mA  
260 = 260 mA

## Typical Part Marking



### 6-MONTH PERIOD CODES:

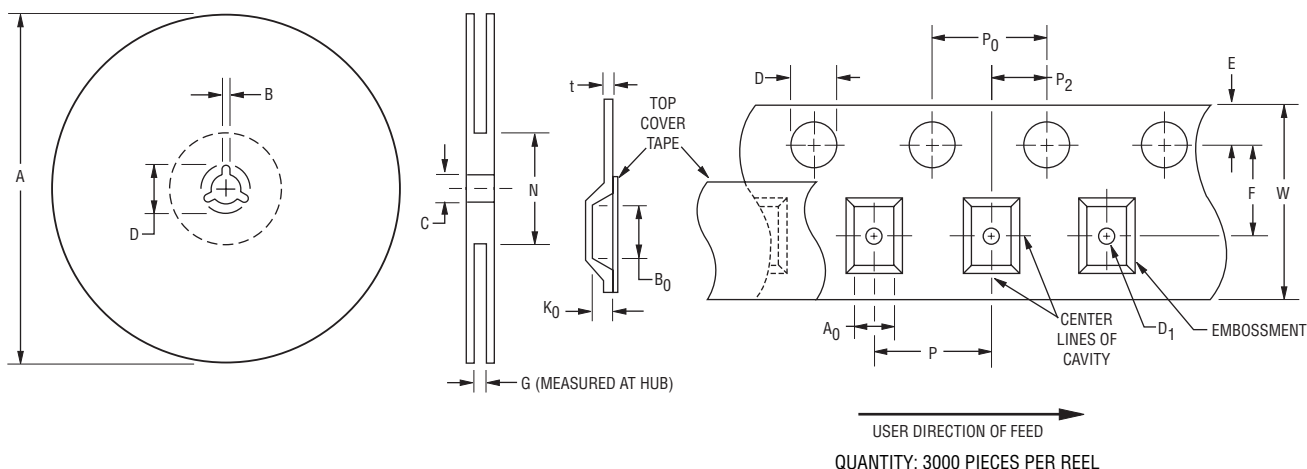
A = JAN-JUN 2009      C = JAN-JUN 2010      E = JAN-JUN 2011  
B = JUL-DEC 2009      D = JUL-DEC 2010      F = JUL-DEC 2011

### EXAMPLE: ARBC

- 1ST DIGIT 'A' = JAN-JUN 2009  
- 2ND DIGIT 'R' = WEEK 18; WEEK OF APRIL 27  
- 3RD & 4TH DIGITS 'BC' = LOT SPECIFIC INFORMATION

\*TRANSITION FROM FULTEC TRADEMARK AND LOT CODE TO BOURNS TRADEMARK AND DATE CODE IN 2009.

## Packaging Specifications (per EIA468-B)



| Device     | A                |                     | B             |               | C              |                | D              |      | G              | N               |
|------------|------------------|---------------------|---------------|---------------|----------------|----------------|----------------|------|----------------|-----------------|
|            | Min.             | Max.                | Min.          | Max.          | Min.           | Max.           | Min.           | Max. | Ref.           | Ref.            |
| C650, C850 | 326<br>(.12.835) | 330.25<br>(.13.002) | 1.5<br>(.059) | 2.5<br>(.098) | 12.8<br>(.504) | 13.5<br>(.531) | 20.2<br>(.795) | -    | 16.5<br>(.650) | 102<br>(.4.016) |

| Device     | A0            |               | B0             |                | D             |               | D1            |               | E              |                | F              |                |
|------------|---------------|---------------|----------------|----------------|---------------|---------------|---------------|---------------|----------------|----------------|----------------|----------------|
|            | Min.          | Max.          | Min.           | Max.           | Min.          | Max.          | Min.          | Max.          | Min.           | Max.           | Min.           | Max.           |
| C650, C850 | 4.2<br>(.165) | 4.4<br>(.173) | 8.45<br>(.333) | 8.65<br>(.341) | 1.5<br>(.059) | 1.6<br>(.063) | 1.5<br>(.059) | -             | 1.65<br>(.065) | 1.85<br>(.073) | 7.4<br>(.291)  | 7.6<br>(.299)  |
| Device     | K0            |               | P              |                | P0            |               | P2            |               | t              |                | W              |                |
|            | Min.          | Max.          | Min.           | Max.           | Min.          | Max.          | Min.          | Max.          | Min.           | Max.           | Min.           | Max.           |
| C650, C850 | 1.1<br>(.043) | 1.3<br>(.051) | 7.9<br>(.311)  | 8.1<br>(.319)  | 3.9<br>(.159) | 4.1<br>(.161) | 1.9<br>(.075) | 2.1<br>(.083) | 0.25<br>(.010) | 0.35<br>(.014) | 15.7<br>(.618) | 16.3<br>(.642) |

DIMENSIONS:  $\frac{\text{MM}}{(\text{INCHES})}$

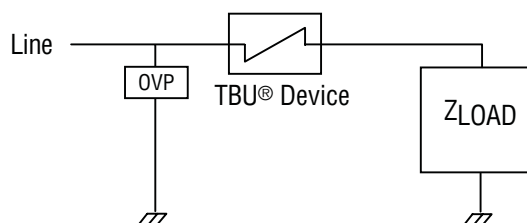
Specifications are subject to change without notice.  
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## C650 and C850 Series TBU® High-Speed Protectors

**BOURNS®**

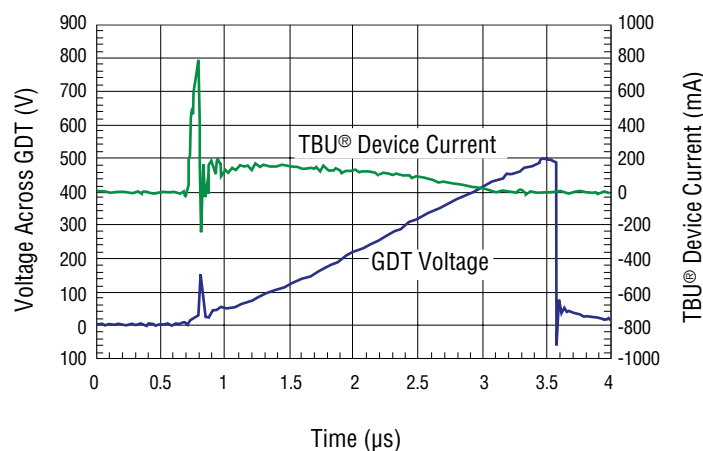
### Reference Application

The C-series devices are general protectors that can be used in a variety of applications. The basic operation of the device will be demonstrated using the single line application shown in the figure below. The test circuit was subjected to a 1000 V, 10/700  $\mu$ s surge waveform. The devices used were the TBU-C850-100-WH and a 2031-42T-SM-RPLF GDT (OVP) with a 10 ohm resistor for the load impedance.



*General Application Circuit*

The graph below shows the waveforms for the voltage across the overvoltage protector (GDT) and the current through the TBU® device. As the input line voltage increases, the current through the TBU® device increases rapidly until the trip current is reached. Due to finite reaction time for fast transients, the peak level may exceed the low frequency data sheet maximum for a very short period, typically  $\sim 100$  ns. After this initial overshoot, the TBU® device will transition to the protected state, setting the current to the nominal current limiting level ( $\sim 150$  mA for this example). The TBU® device will then reduce the current down to its very low quiescent level of 1 mA, typically. As the input line voltage increases to about 500 V, the GDT is triggered, reducing the input line voltage to a very low level which prevents the TBU® device from being subjected to a voltage level which exceeds its maximum rating (850 V in this example). The TBU® High-Speed Protector and the GDT will remain in these states until the surge ends, which is about 700  $\mu$ s later in this example. Only the first 4  $\mu$ s of the surge are shown in the graph. For surges or AC voltages below the GDT breakover voltage, the GDT will not activate, and the TBU® device will stay in the protecting mode, blocking high voltages from the protected equipment.



*TBU-C850-100-WH Response to a 1000 V, 10/700  $\mu$ s Surge*

REV. 06/12

"TBU" is a registered trademark of Bourns, Inc. in the United States and other countries.

Specifications are subject to change without notice.

Customers should verify actual device performance in their specific applications.



## О компании

ООО "ТрейдЭлектроникс" - это оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов. Реализуемая нашей компанией продукция насчитывает более полумиллиона наименований.

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Наличие собственной эффективной системы логистики обеспечивает надежную поставку продукции по конкурентным ценам в точно указанные сроки.

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Срок поставки из **Азии – от 10 дней.**

Благодаря развитой сети поставщиков, помогаем в поиске и приобретении экзотичных или снятых с производства компонентов.

Предоставляем спец цены на элементы для создания инженерных сэмплов.

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- Широкий ассортимент
- Минимальные сроки поставок
- Техническая поддержка
- Подбор комплектации
- Индивидуальный подход
- Гибкое ценообразование

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## Перечень производителей, продукцию которых мы поставляем на российский рынок





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С уважением,

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8 (495)668-30-28 доб 169

manager28@tradeelectronics.ru

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